


DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

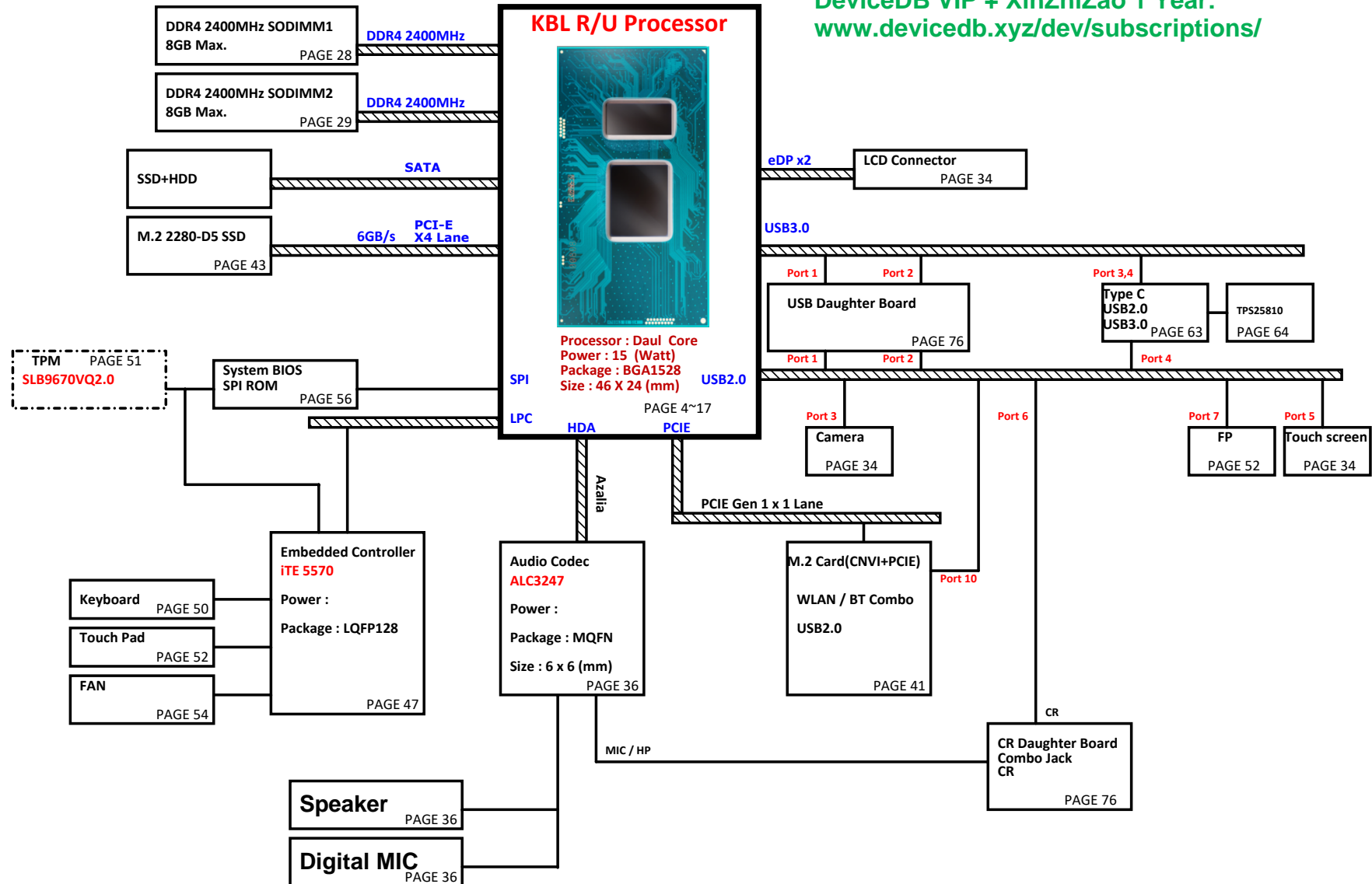
OPK 15" Intel KBL R/U Platform Block Diagram

QUANTA OP5-OP5A

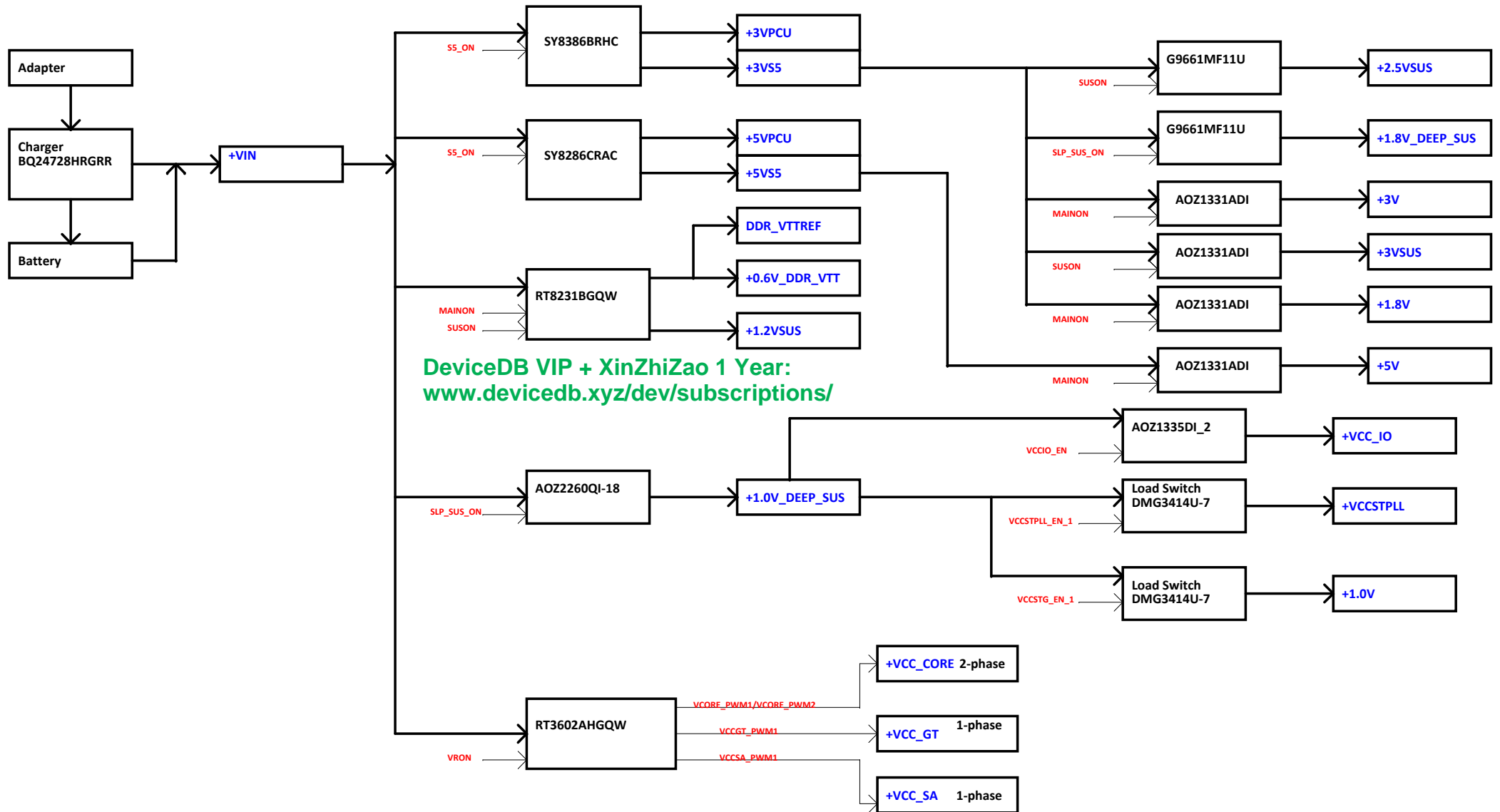
 NB5	PROJECT : OP5A Quanta Computer Inc.		
	Size	Document Number	Rev
	I2C Path		1A
Date: Wednesday, April 03, 2019 Sheet 8 of 90			

LAYER 1 : TOP
 LAYER 2 : SGND
 LAYER 3 : IN1
 LAYER 4 : IN2
 LAYER 5 : SGND1
 LAYER 6 : BOT

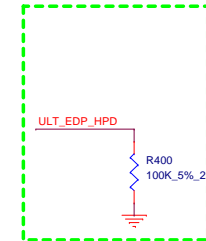
DeviceDB VIP + XinZhiZao 1 Year:
www.device-db.xyz/dev/subscriptions/



Power Diagram



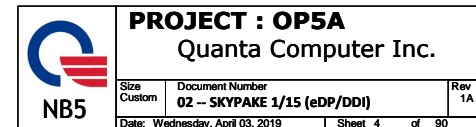
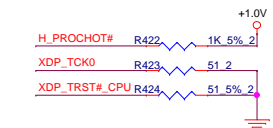
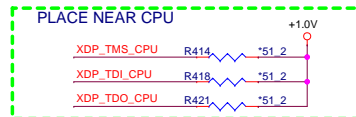
DeviceDB VIP + XinZhiZao 1 Year:
www.devicedb.xyz/dev/subscriptions/



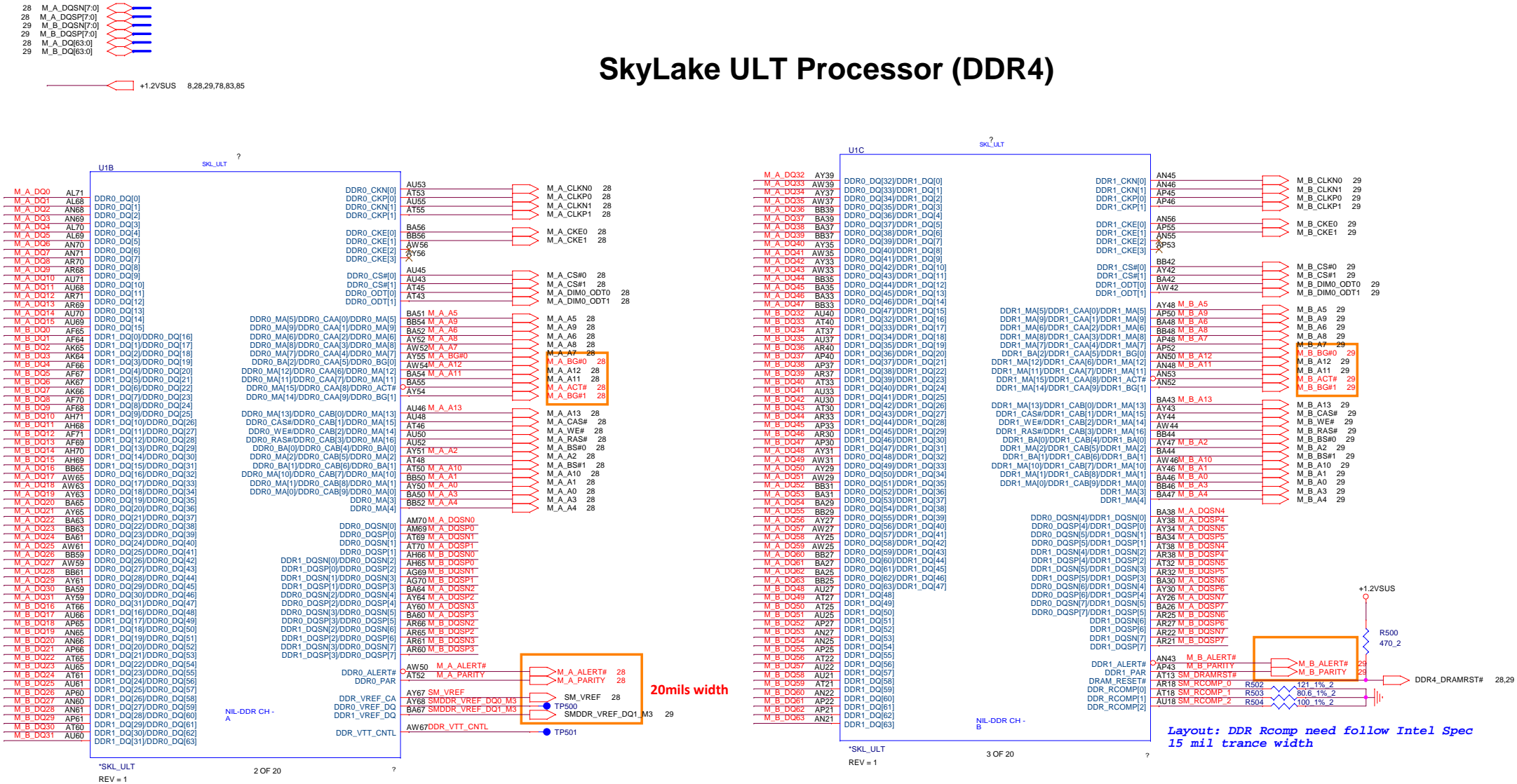
eDP_COMPIO and ICOMPO signals should be shorted near balls and routed with typical impedance <25 mohms



Processor pull-up (CPU)
TO BE REPLACED WITH 1K OHMS FOR SKL .
470 OHM IS FOR I/P

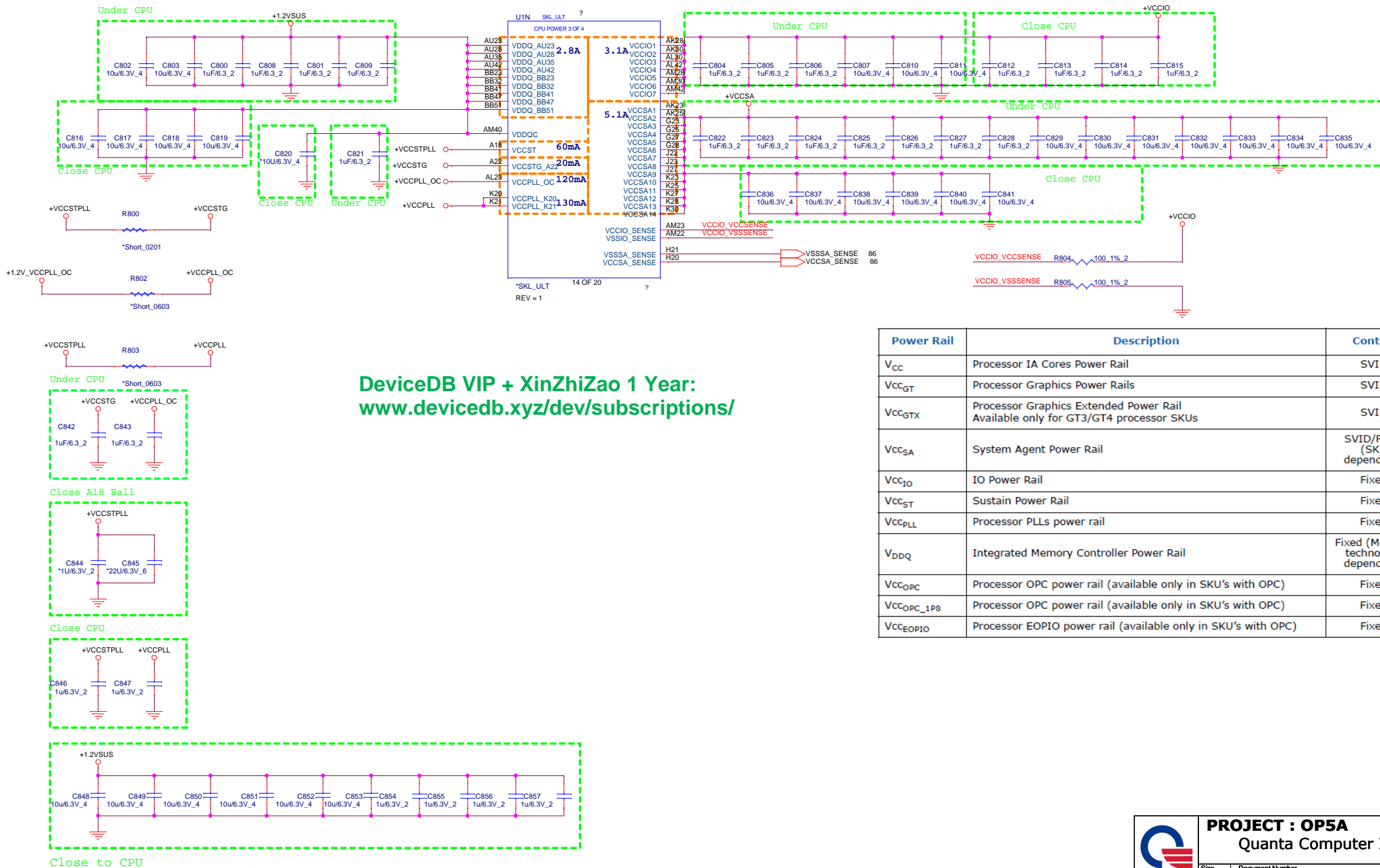


SkyLake ULT Processor (DDR4)






+VCCSTPLL 4.7,11,85,86
 +VCCSA 78,86,88
 +1.2VSUS 5,28,29,78,83,85
 +1.0V_DEEP_SUS 11,15,17,78,84,85
 +1.0V 4,6,47,85
 +3VPCU 15,36,41,47,50,54,63,76,81,82,90



DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

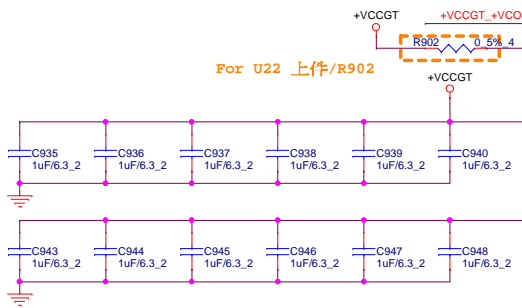
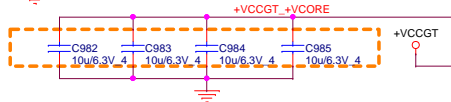
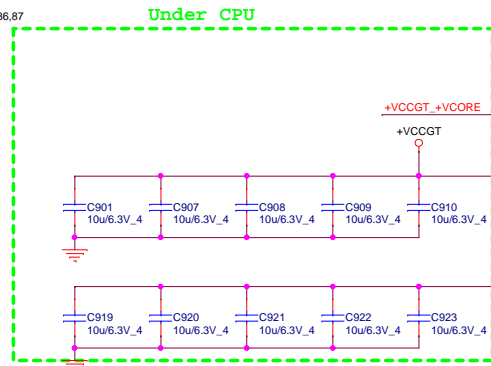
Power Rail	Description	Control
V _{CC}	Processor IA Cores Power Rail	SVID
V _{CCGT}	Processor Graphics Power Rails	SVID
V _{CCGTX}	Processor Graphics Extended Power Rail Available only for GT3/GT4 processor SKUs	SVID
V _{CCSA}	System Agent Power Rail	SVID/Fixed (SKU dependent)
V _{CCIO}	IO Power Rail	Fixed
V _{CCST}	Sustain Power Rail	Fixed
V _{CCPLL}	Processor PLLs power rail	Fixed
V _{DDQ}	Integrated Memory Controller Power Rail	Fixed (Memory technology dependent)
V _{CCOPC}	Processor OPC power rail (available only in SKU's with OPC)	Fixed
V _{CCOPC_1PB}	Processor OPC power rail (available only in SKU's with OPC)	Fixed
V _{CC_EOPIO}	Processor EOPIO power rail (available only in SKU's with OPC)	Fixed



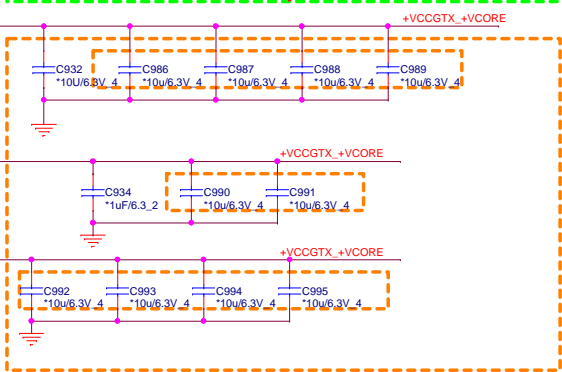
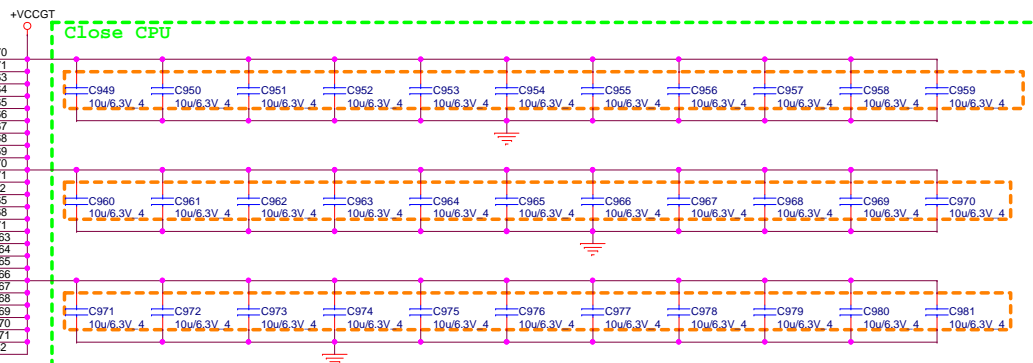
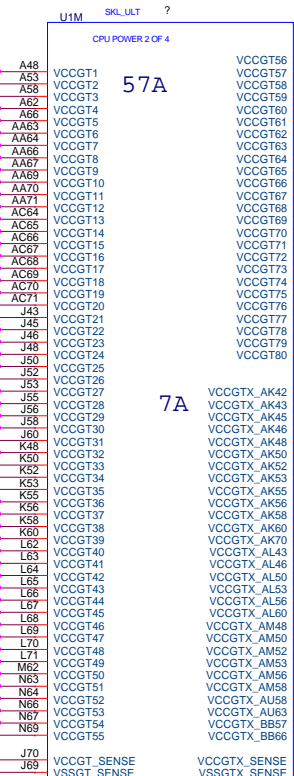
PROJECT : OP5A
Quanta Computer Inc.

Size Custom	Document Number 06 - SKYLAKE 5/15 (POWER-2)	Rev 1A
Date: Wednesday, April 03, 2019		Sheet 8 of 90

+VCCGT 78.86,88
+VCC_CORE 7.78,86,87



86 VCCGT_SENSE
86 VSSGT_SENSE

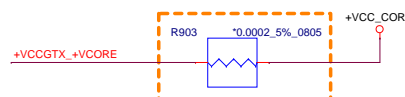
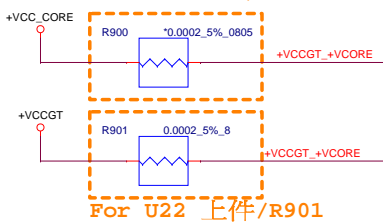


*SKL_ULT 13 OF 20
REV = 1

DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

- 1.U22---C932/C986/C987/C988/C989/C934/C990/C991/C992/C993/C994/C995/C982/C983/C984/C985 不上件
- 2.U42---C932/C986/C987/C988/C989/C934/C990/C991/C992/C993/C994/C995/C982/C983/C984/C985 上件

For R-U42 上件/R900

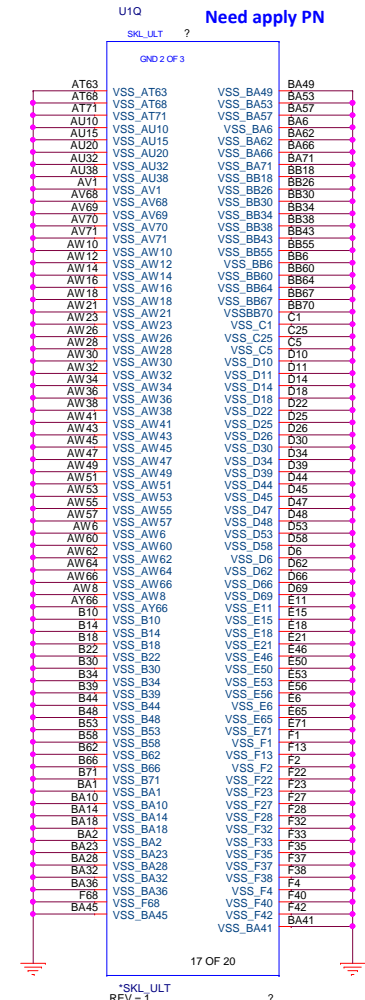
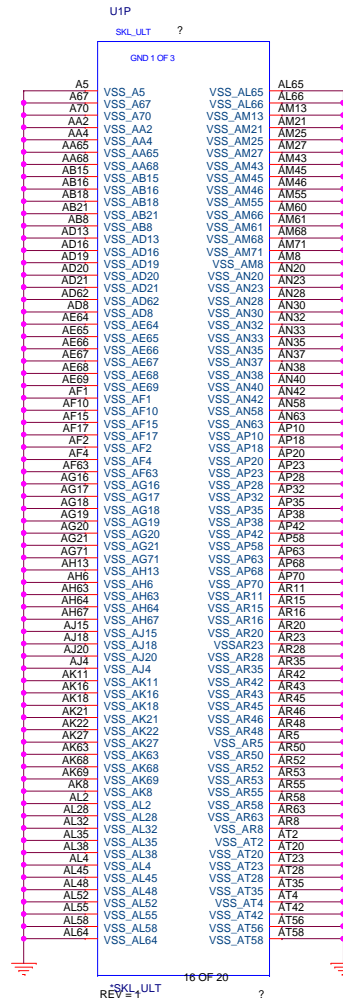
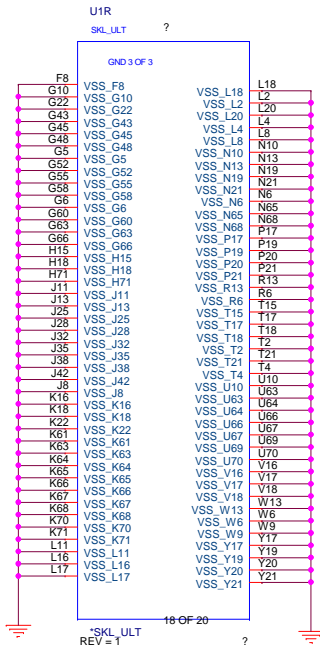



- 1.U22--->R903 不上件
- 2.U42--->R903 上件

Power Rail	Description	Control
V _{CC}	Processor IA Cores Power Rail	SVID
V _{CCGT}	Processor Graphics Power Rails	SVID
V _{CCGTx}	Processor Graphics Extended Power Rail Available only for GT3/GT4 processor SKUs	SVID
V _{CCSA}	System Agent Power Rail	SVID/Fixed (SKU dependent)
V _{CCIO}	IO Power Rail	Fixed
V _{CCST}	Sustain Power Rail	Fixed
V _{CCPLL}	Processor PLLs power rail	Fixed
V _{DDQ}	Integrated Memory Controller Power Rail	Fixed (Memory technology dependent)
V _{CCOPC}	Processor OPC power rail (available only in SKU's with OPC)	Fixed
V _{CCOPC_1P8}	Processor OPC power rail (available only in SKU's with OPC)	Fixed
V _{CCEOPIO}	Processor EOPIO power rail (available only in SKU's with OPC)	Fixed

PROJECT : OP5A
Quanta Computer Inc.

Size Custom	Document Number 07 - SKYLAKE 6/15 (POWER-3)	Rev 1A
Date: Wednesday, April 03, 2019	Sheet 9	of 90

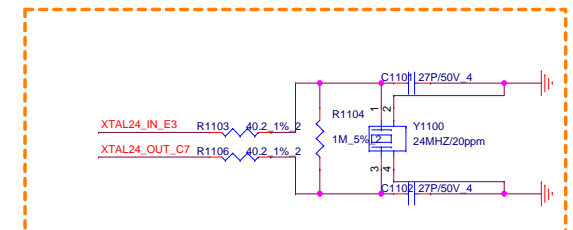




PROJECT : OP5A
Quanta Computer Inc.

Size Custom	Document Number 08 - SKYLAKE 7/15 (GND)	Rev 1A
Date: Wednesday, April 03, 2019 Sheet 10 of 90		

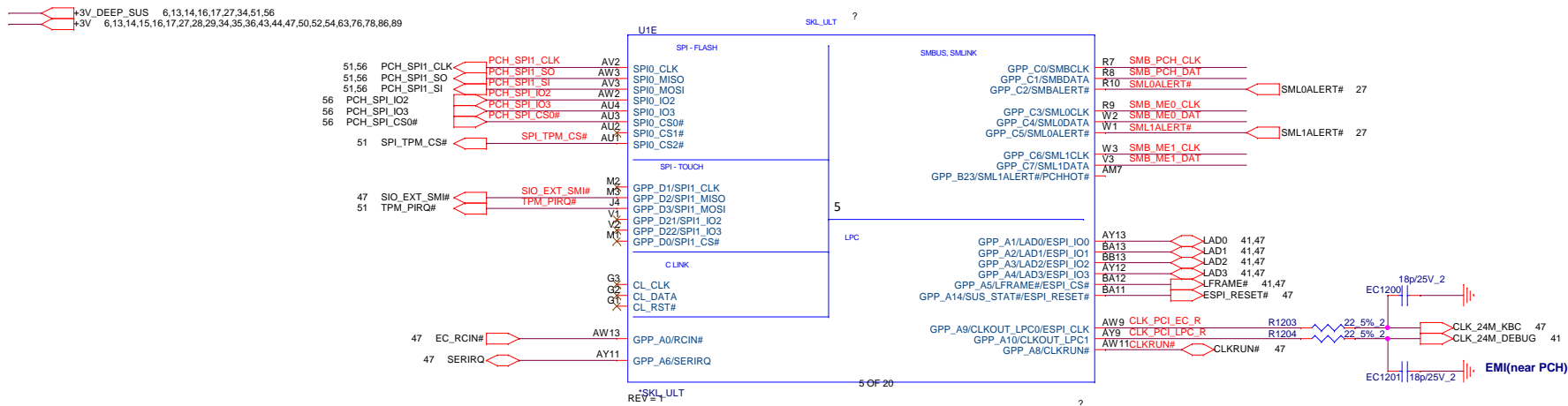
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/



(I) KBL R U42 上件
(II) KBL-U22 不上件

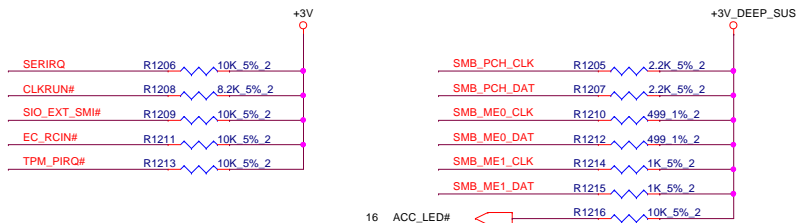
Processor Debugging: The DP signals have a default value of 1, if not terminated on the board.			
	1	0	Circuit
CFG3 (Physical Debug Enable) DFX_Privacy	Disable:	Enable: Set DFX Enable in DFX interface MSR	
CFG4 (DP Presence Strap)	Disable; No physical DP attached to eDP	Enable; An ext DP device is connected to eDP	

DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/

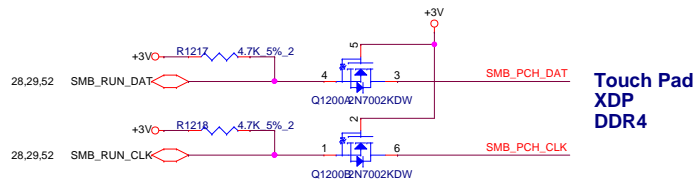



GPIO Pull UP

DeviceDB VIP + XinZhiZao 1 Year:
www.devicedb.xyz/dev/subscriptions/

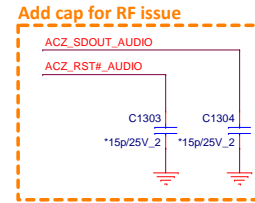


SMBus/Pull-up(CLG)

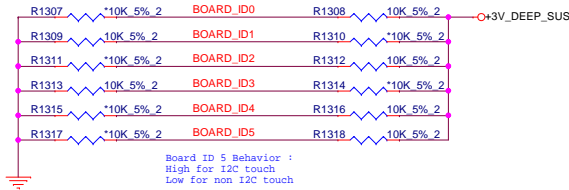
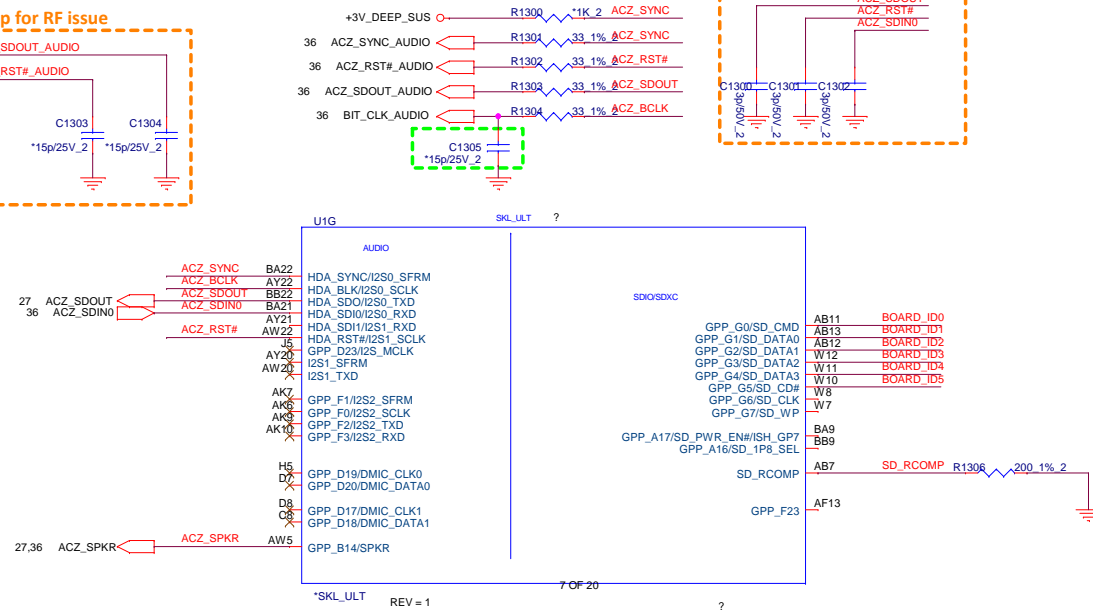


	PROJECT : OP5A		
	Quanta Computer Inc.		
	Size Custom	Document Number 10 - SKYLAKE 09/15(SPI/LPC/SM)	Rev 1A
Date: Wednesday, April 03, 2019		Sheet 2	of 90

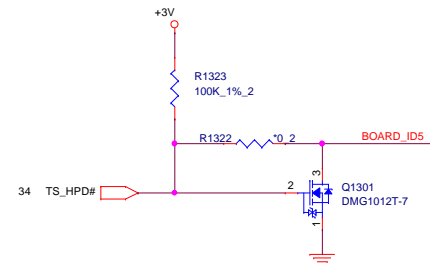
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/



HDA Bus(CLG)



for T/S Determination

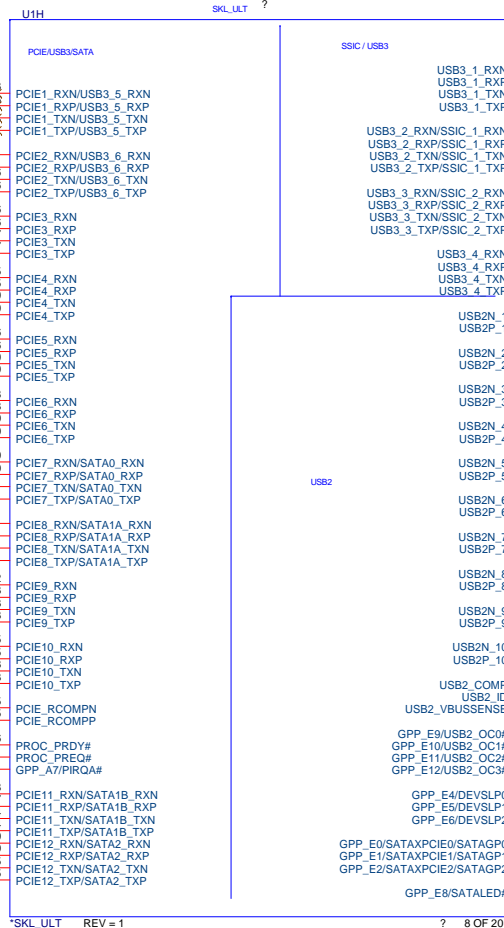


WHL/KBL	Board ID 5	Board ID 4	Board ID 3	BOARD_ID[2:1:0]	
Model	ID5	ID4	ID3	ID2 ID1	ID0
Definition	1 : active by eDP cable 0 : Reserve Active By eDP Cable	1 : USB touch 0 : I2C touch	Reserved =0	100 : 15 WHL 101 : 15 KBL 110 : 15 GML	000 : 14 WHL 001 : 14 KBL 010 : 14 GML

PROJECT : OP5A
Quanta Computer Inc.

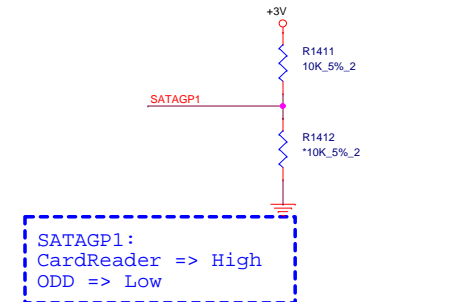
Size Custom	Document Number 11 - SKYLAKE 10/15(HDA)	Rev 1A
Date: Wednesday, April 03, 2019	Sheet 3	of 90

+3V 6,12,13,15,16,17,27,28,29,34,35,36,43,44,47,50,52,54,63,76,78,86,89
+3V_DEEP_SUS 6,12,13,16,17,27,34,51,56




PCI-E Port Mapping Table			
PCI-E Port	Function	CLK RQ Port	Function
Port1	NA	Port0	NA
Port2	NA	Port1	NC
Port3	NA	Port2	SSD
Port4	NA	Port3	WLAN
Port5	WLAN	Port4	LAN
Port6	LAN	Port5	Un-used
Port7	HDD		
Port8	Co-lay SSD		
Port9	SSD		
Port10	SSD		
Port11	SSDx2		
Port12	SSDx2/ SATA2		

USB3.0 Port Mapping Table	
USB3.0	Function
PORT-1	USB3.0 Small Board
PORT-2	USB3.0 Small Board
PORT-3	Type C
PORT-4	Type C



USB2.0 Port Mapping Table	
USB2.0	Function
PORT-1	Cobime USB3.0 MB-1
PORT-2	Cobime USB3.0 Small Board
PORT-3	Camera
PORT-4	Type C
PORT-5	Touch Screen
PORT-6	CardReader
PORT-7	Finger print
PORT-8	NC
PORT-9	NC
PORT-10	BT



PROJECT : OP5A
Quanta Computer Inc.

Size Custom	Document Number 11 - SKYLAKE 10/15(HDA)	Rev 1A
Date: Wednesday, April 03, 2019	Sheet4	of 90

USB3.0 Small Board
DB 1SPD TypeA UP
USB3.0 Small Board
DB 1SPD TypeA DN

MB Type C#1a

Combo USB3.0 Small Board UP

Combo USB3.0 Small Board DN

Camera

Type C

Touch Screen

CR

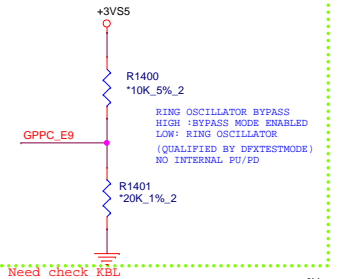
FP

BT

PLACE USB2_COMP Res. WITHIN 500 MILS
FROM USB2_COMP PIN WITH
TRACE IMPEDANCE LESS THAN 0.5 OHMS

If OTG is not implemented on the platform,
then USB2_ID and USB2_VBUSSENSE should both
be connected to ground.

GPIO35:
SSD SATA IF => High
SSD PCIE IF => Low



DIS ONLY

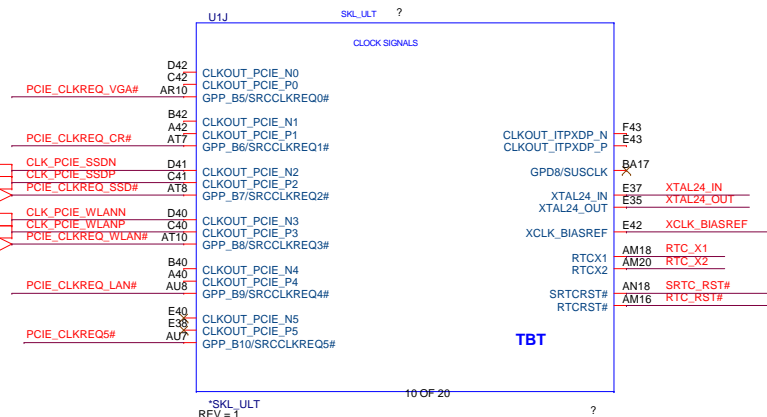
2016/9/7
For Base-U the SATA1B/SATA2 delete

DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

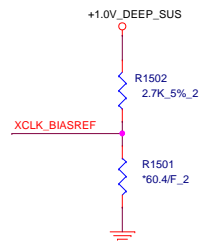
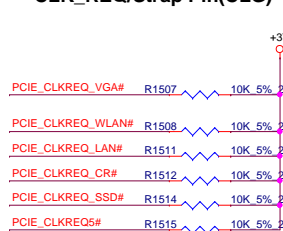
BAT_RTC 6,17,34,81,90
+3V 6,12,13,14,16,17,27,28,29,34,35,36,43,44,47,50,52,54,63,76,78,86,89
+1.0V_DEEP_SUS 11,17,78,84,85

SSD

WLAN



CLK_REQ/Strap Pin(CLG)

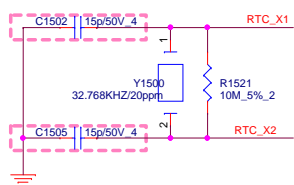


RTC Circuitry(RTC) 30mils

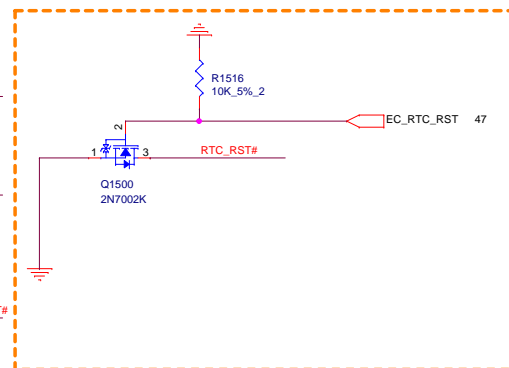
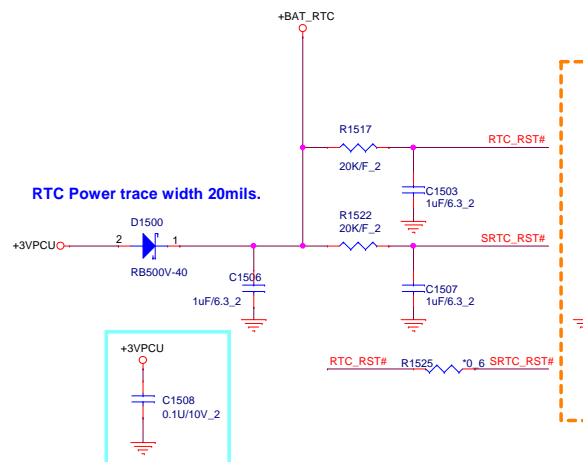
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

RTC Clock 32.768KHz

main:BQ332768111零件高度0.75+/-10% mm

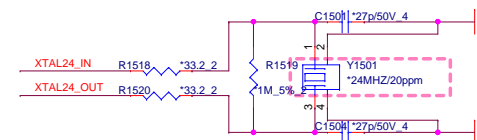


RTC Power trace width 20mils.



External Crystal

The 24 MHz (50 Ohm ESR) XTAL used for Skylake-U needs to be replaced by 38.4 MHz (30 Ohm ESR) XTAL for Cannonlake-U.



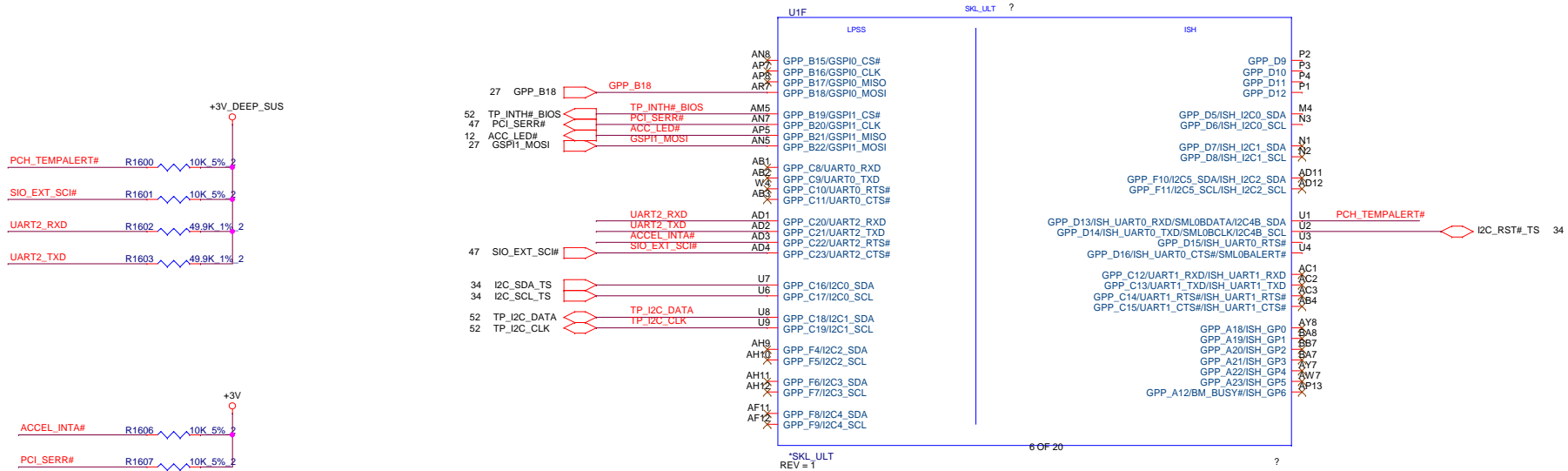
Non-stuff on KBL R U42
stuff on KBL-U22


NB5	PROJECT : OP5A Quanta Computer Inc.		
	Size Custom	Document Number 13 - SKYLAKE 12/15 (CLK/EMMC)	Rev 1A
	Date: Wednesday, April 03, 2019	Sheet 5	of 90

DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/

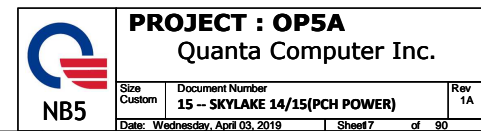
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

Skylake (GPIO)



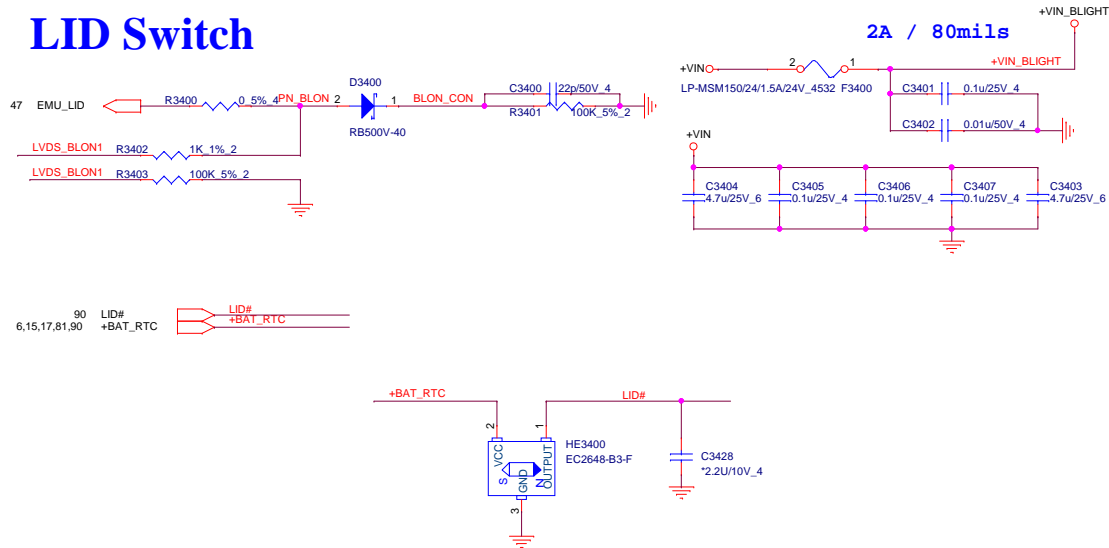
	PROJECT : OP5A Quanta Computer Inc.		
	Size Custom Document Number 14 -- SKYLAKE 13/15 (GPIO)		Rev 1A
	Date: Wednesday, April 03, 2019	Sheet6 of 90	

6,12,13,14,16,27,34,51,56	+3V_DEEP_SUS	
11,15,78,84,85	+1.0V_DEEP_SUS	
11,47,76,84,89	+1.8V_DEEP_SUS	
6,15,34,81,90	+BAT_RTC	
6,14,41,47,63,78,82,83,84,85,89	+3VS5	



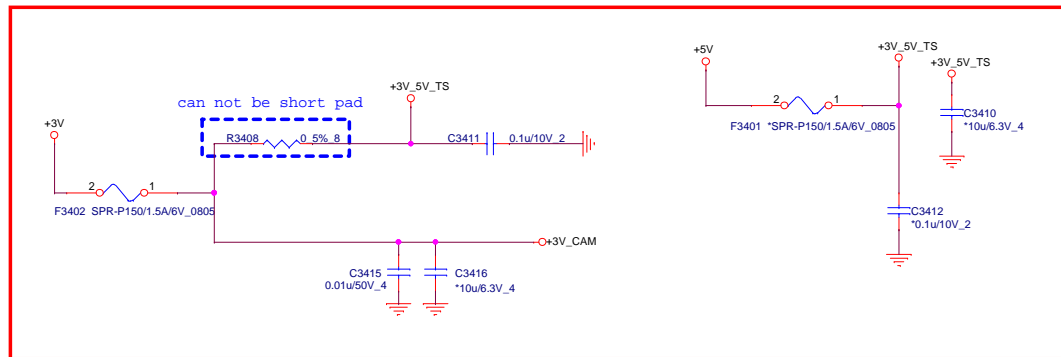


LID Switch

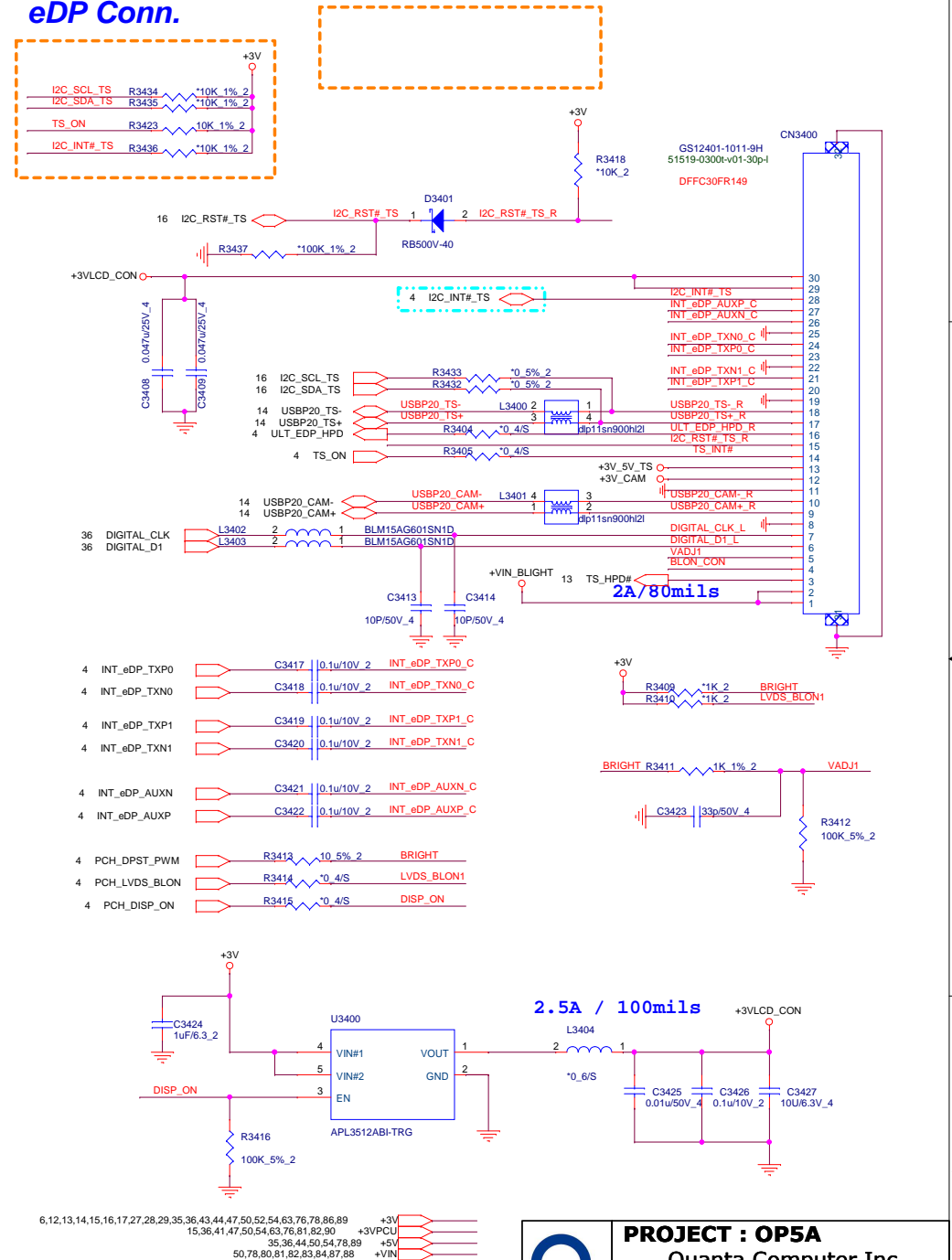


DeviceDB VIP + XinZhiZao 1 Year:
www.deviceb.xyz/dev/subscriptions/

Touch screen



eDP Conn.



PROJECT : OP5A
Quanta Computer Inc.

Size	Document Number	Document Number	Rev
Custom	34 - eDP CONN/LID/CAM/D-MIC/TS	1A	

Date: Wednesday, April 03, 2019 Sheet4 of 90

MIC2-VREFO R3602 2.2K 2

SLEEVE_L R3601 *0.4/S

EXT_MIC_L 76

3V


No need headphone amplifier
keep codec pin 10 floating

R3610
*100K_2 Default: None Amp.

+3V or +1.8V, Based on which platform

Close to Speaker
Speaker 4 ohm: 40mils

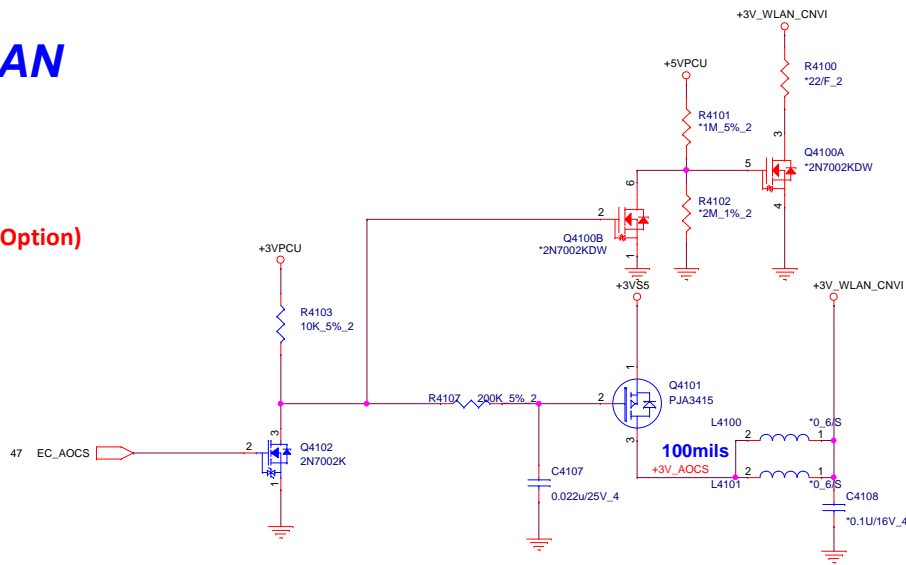
FOR EMI		
ACZ_SDIN0	EC3605	*33p/50V 4
ACZ_SDOUT_AUDIO	EC3606	*10p/50V 4
ACZ_SYNC_AUDIO	EC3607	*10p/50V 4
BIT_CLK_AUDIO	EC3608	*33p/50V 4

	PROJECT : OP5A Quanta Computer Inc.		
	Size Custom	Document Number Codec ALC3258-CG	
	Date: Wednesday, April 03, 2019	Sheet 36 of 90	Rev 1A

DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/

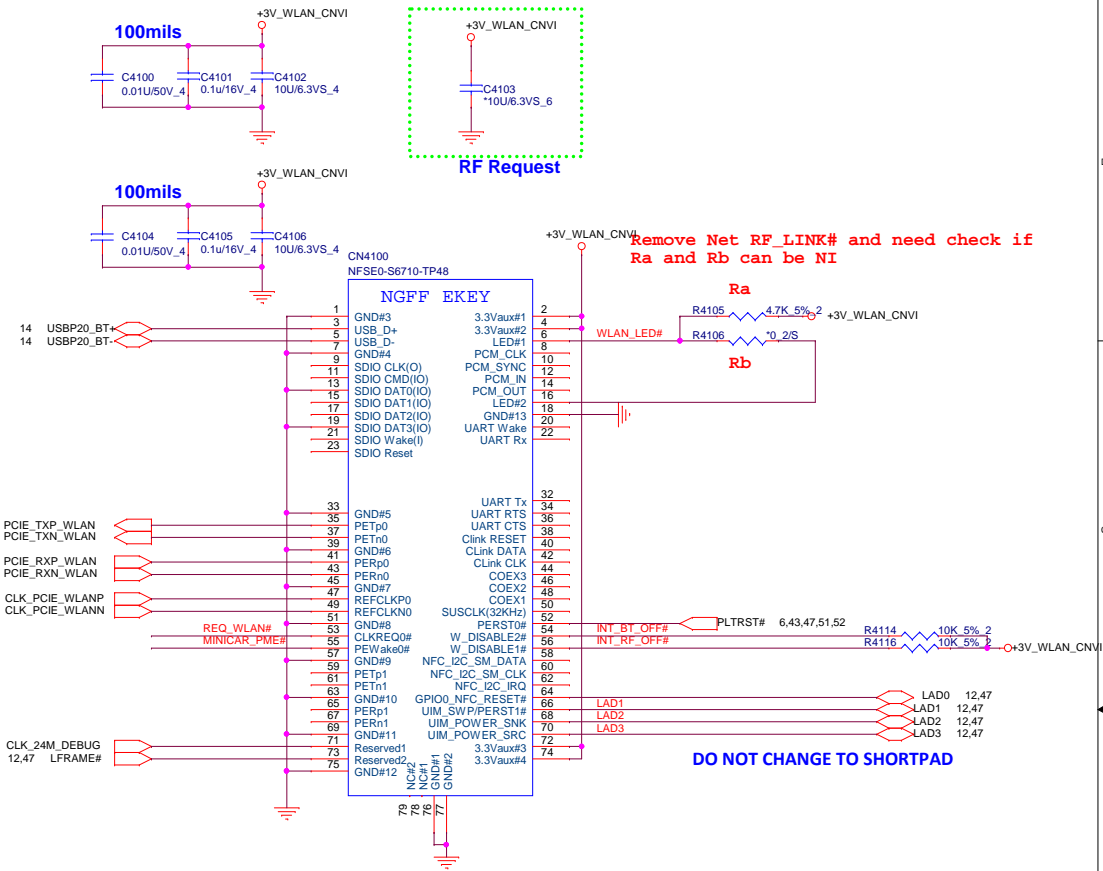
WLAN

Mini Card
WLAN/BT(Optional)



DeviceDB VIP + XinZhiZao 1 Year:
www.device-db.xyz/dev/subscriptions/

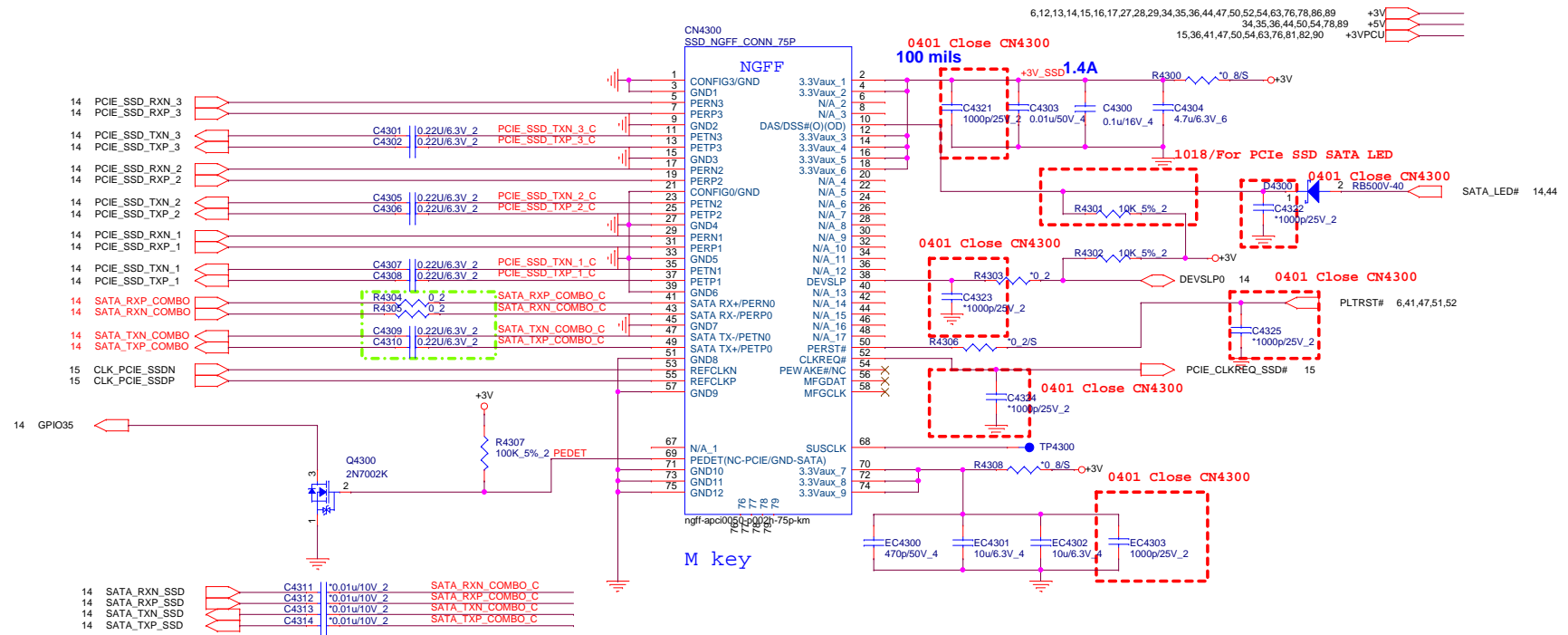
For EMI Suggestion




DO NOT CHANGE TO SHORTPAD

		PROJECT : OP5A	
		Quanta Computer Inc.	
Size Custom	Document Number WLAN(NGFF)	Rev 1A	
Date: Wednesday, April 03, 2019		Sheet 41 of 90	

SATA SSD



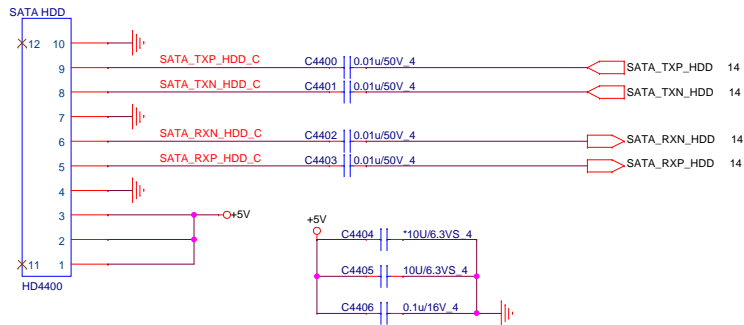
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

		PROJECT : OP5A	
		Quanta Computer Inc.	
Size Custom	Document Number SSD(NGFF)	Rev 1A	
Date: Wednesday, April 03, 2019	Sheet 43	of 90	

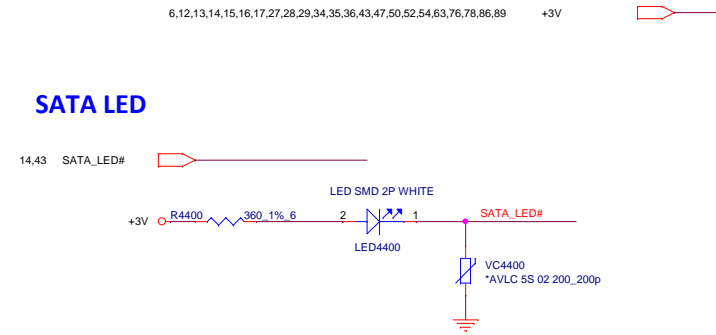
DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/


DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

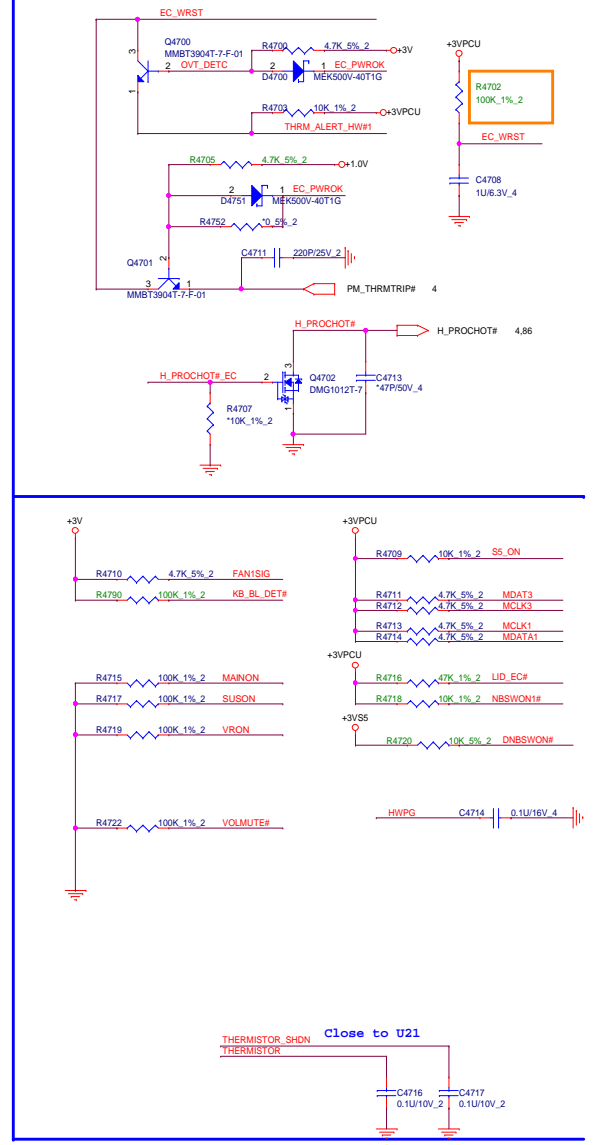
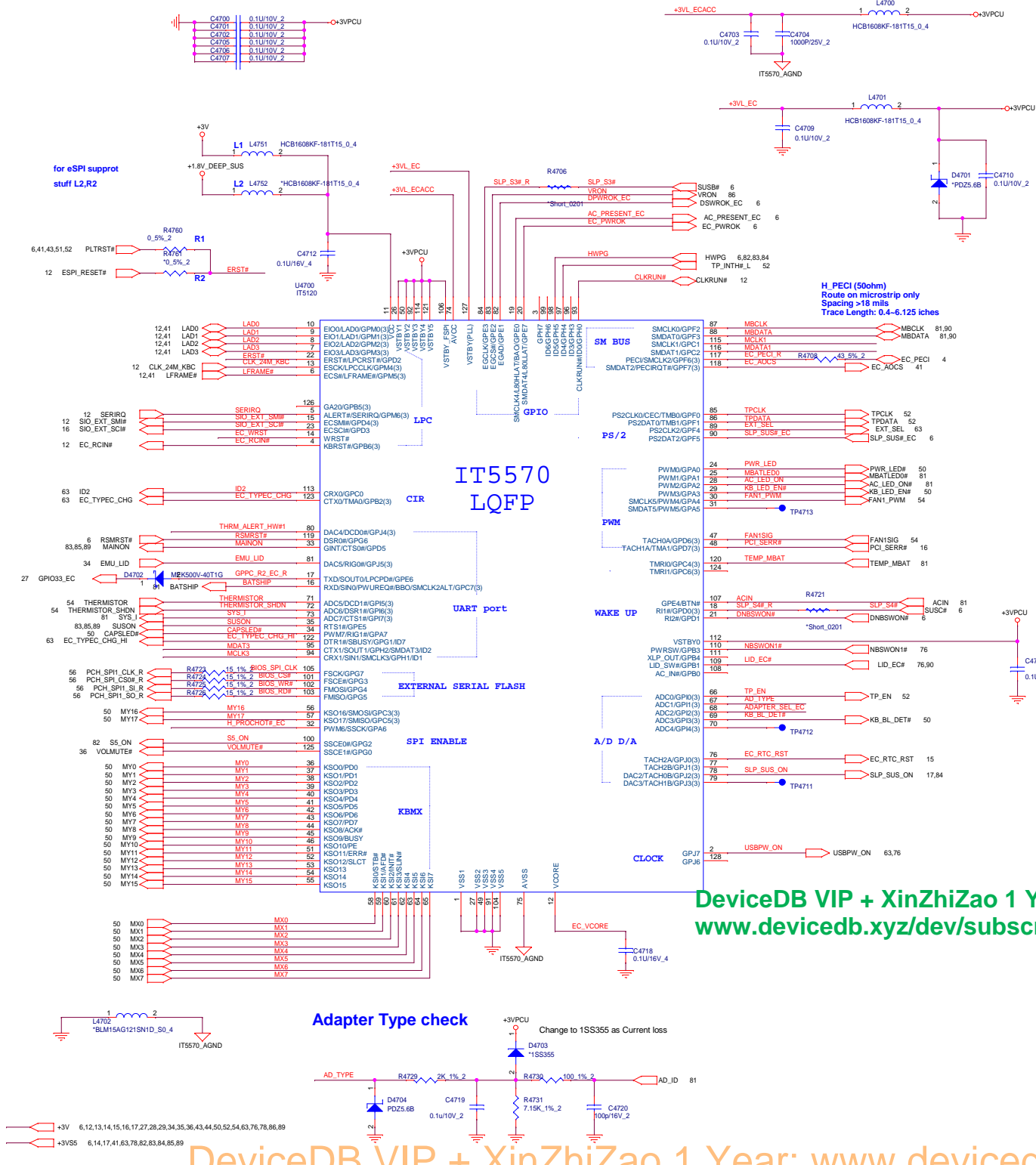
SATA HDD & LED



SATA LED



	PROJECT : OP5A Quanta Computer Inc.		
	Size Custom	Document Number HDD/ODD	Rev 1A
	Date: Wednesday, April 03, 2019 Sheet 44 of 90		

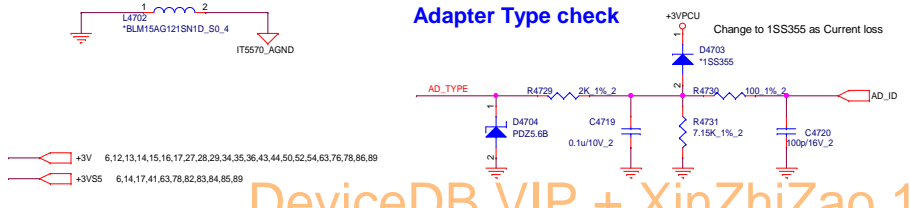


DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/

Adapter select for EC

	Ra	Rb	ADAPTER_SEL_EC	BOM
200W	10K(CS31002FB26)	100K (CS41002FB28)	3V	
150W	10K(CS31002FB26)	100K (CS41002FB28)	3V	
120W	10K(CS31002FB26)	21.5K(CS32152FB09)	2.25V	MAXQ DIS TI
90W	10K(CS31002FB26)	8.25K(CS28252FB15)	1.5V	MAXQ DIS
65W	10K(CS31002FB26)	2.94K(CS22942FB01)	0.75V	DIS
45W	NC	10K(CS31002JB28)	0V	UMA

Adapter Type check



PROJECT : OP5A
Quanta Computer Inc.

Size C

Document Number
EC (IT5570)

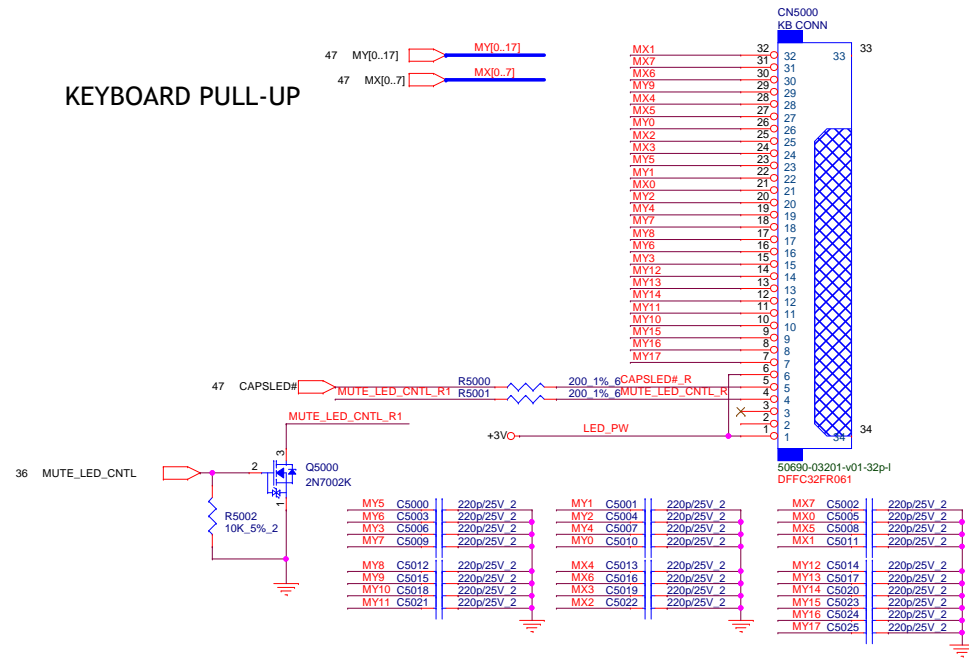
Date: Wednesday, April 03, 2018

Rev 1A

Sheet 47 of 90

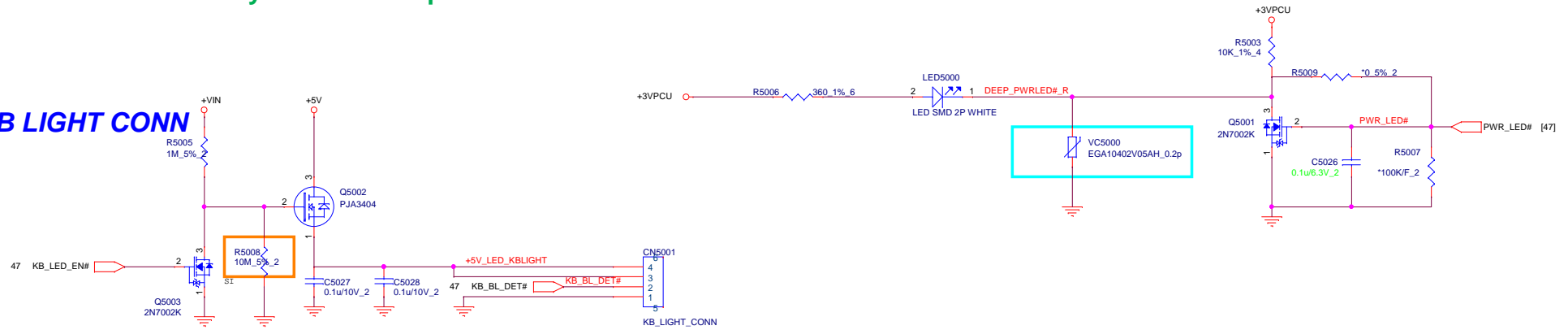
KEYBOARD Con.

KEYBOARD PULL-UP



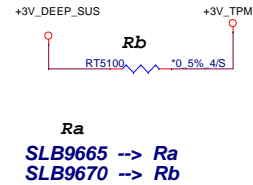
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

KB LIGHT CONN

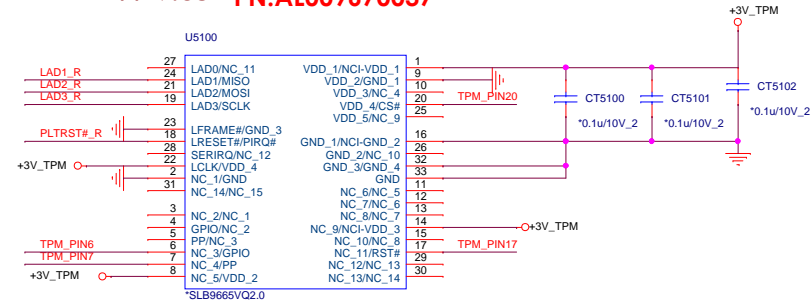


TPM

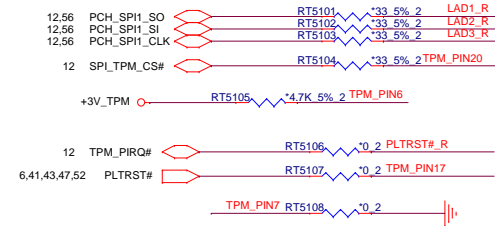
TPM (2.0)



FW 7.83 PN:AL009670037

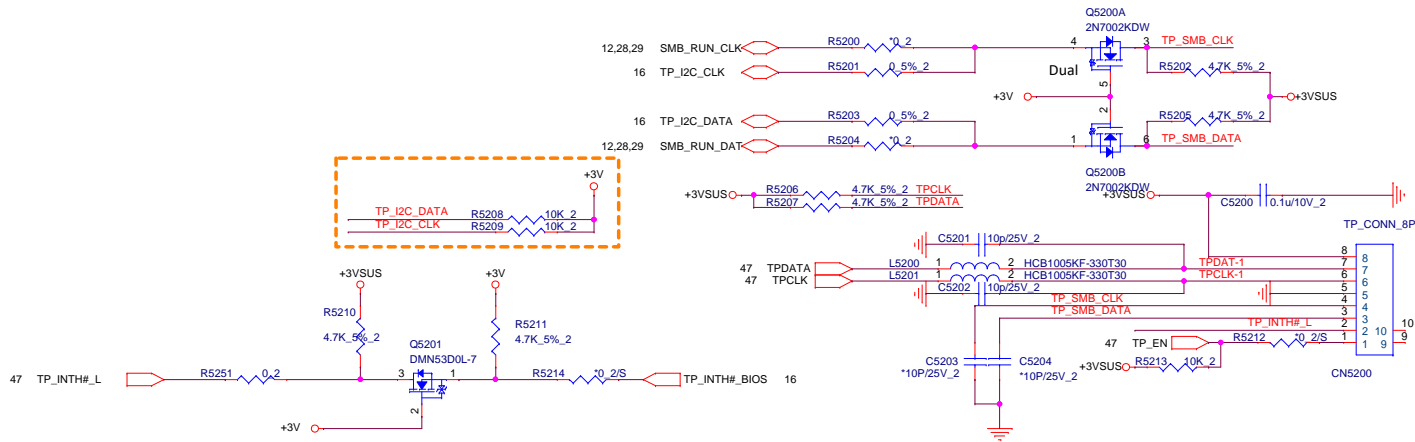


DeviceDB VIP + XinZhiZao 1 Year:
www.device-db.xyz/dev/subscriptions/



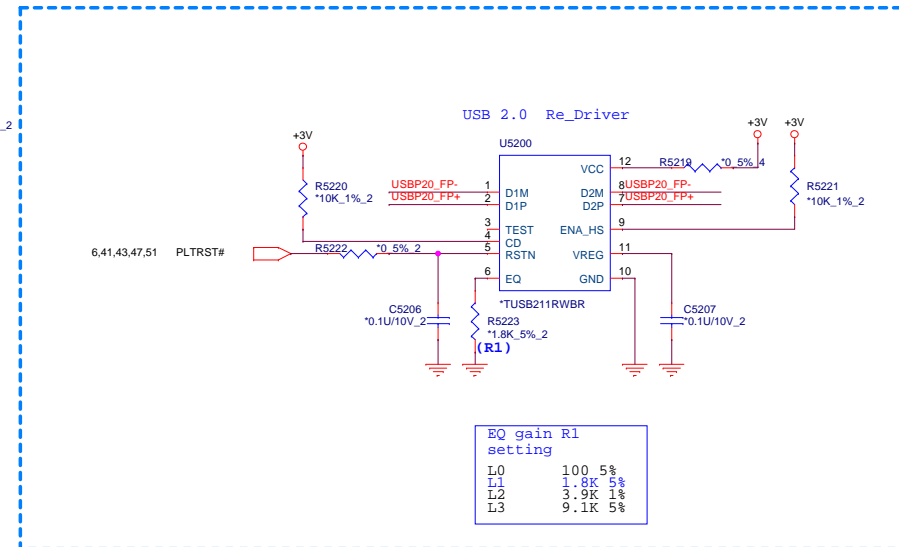
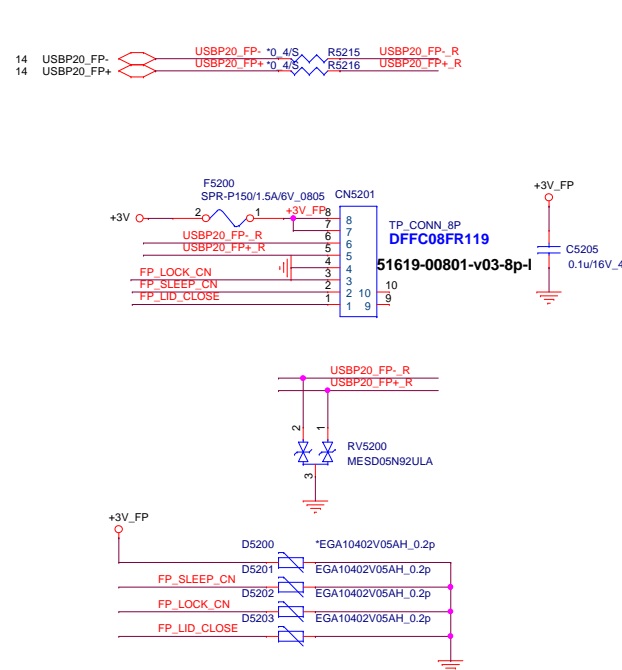
For 9670 stuff

Touch Pad

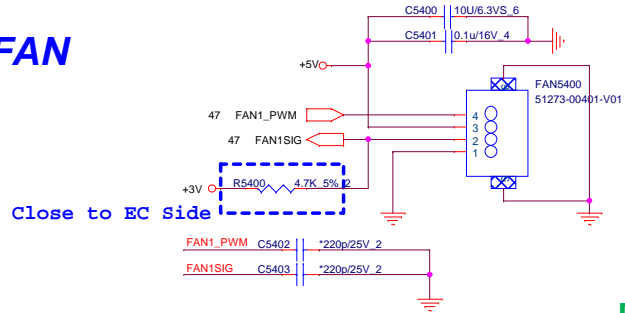


DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

Finger print



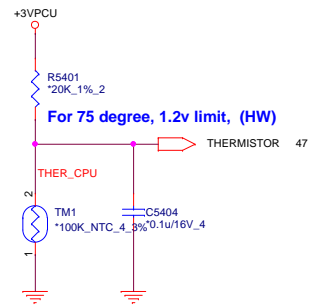
FAN/Thermal

FAN

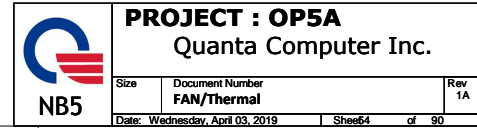
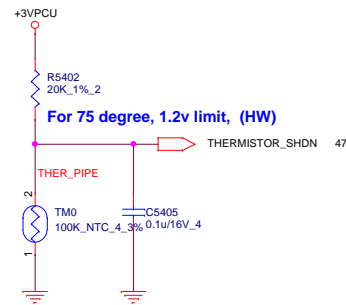
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

IO Thrm Protect

For PIPE USE



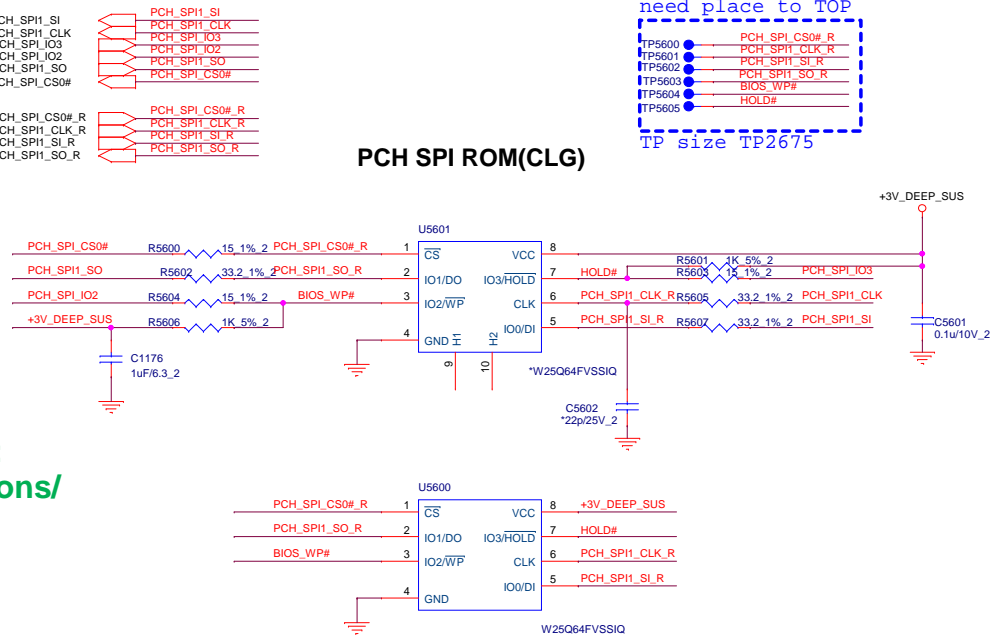
For CPU USE



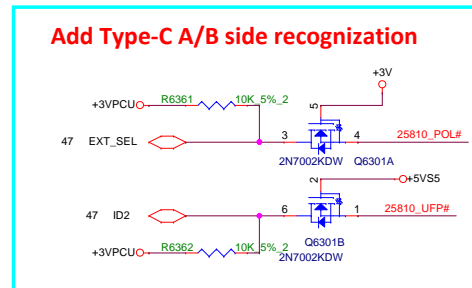
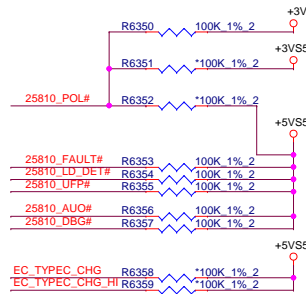
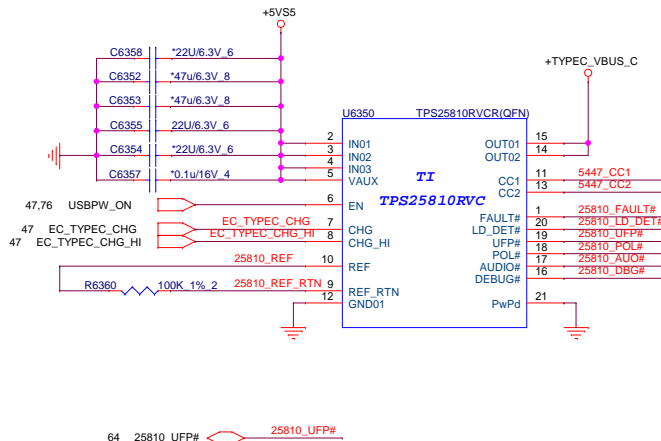
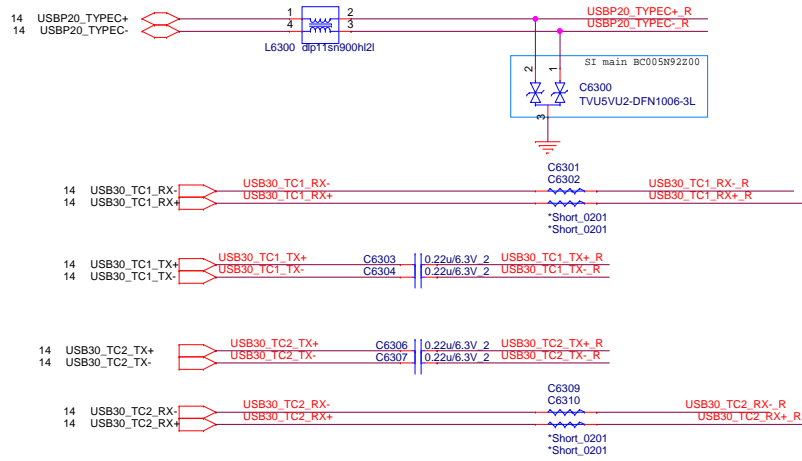
GLK / KBL : _____

Vendor	Size	SO8	
		Vendor PN	QCI PN
GigaDevice	8M	GD25B64CSIGR	AKE2EZN0Q00
Winbond	8M	W25Q64JVSSIQ	AKE3EFP0N07
XMC	8M	XM25QH64AHIGT	AKE3EZP0X00

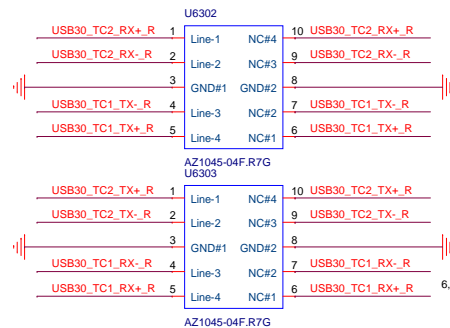
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/



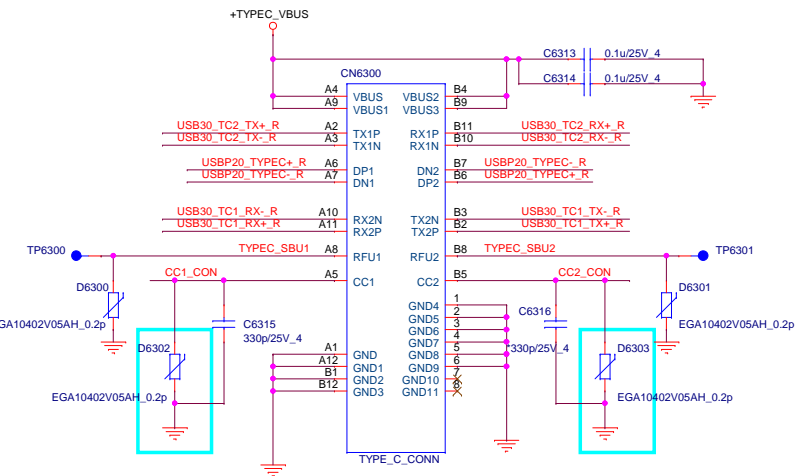
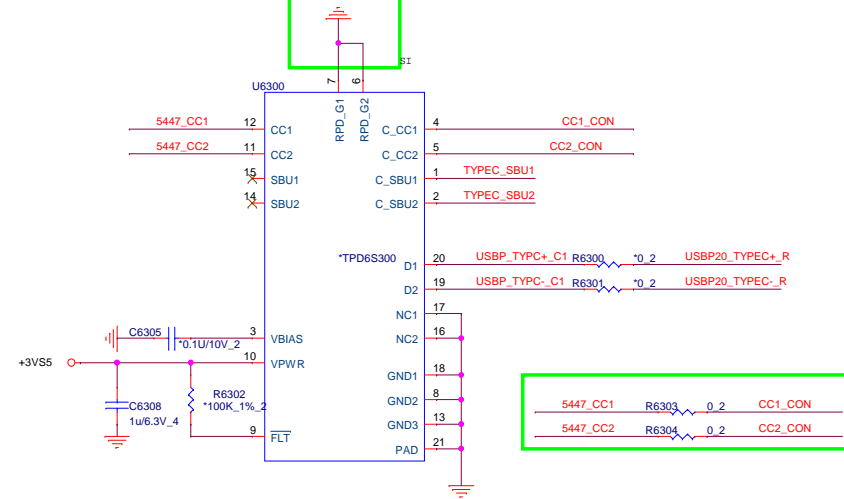
USB2.0



TYPE C USB3.0 ESD




CC/SBU Overvoltage protection



PROJECT : OP5A
Quanta Computer Inc.

Size Custom	Document Number TYPE-C	Rev 1A
Date: Wednesday, April 03, 2019		Sheet 63 of 90

	PROJECT : OP5A Quanta Computer Inc.		
	Size	Document Number	Rev
	TYPE-C Load Switch		1A
Date: Wednesday, April 03, 2019		Sheet 64	of 90

DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/

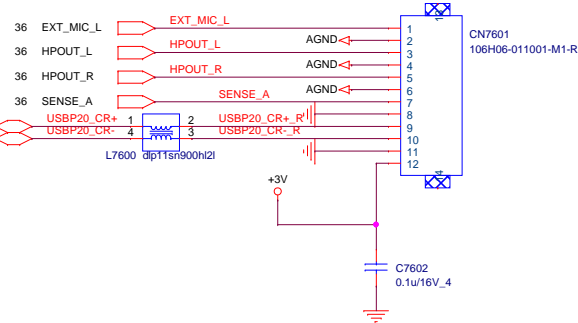
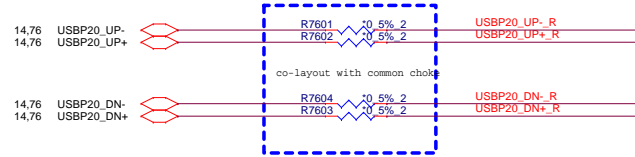
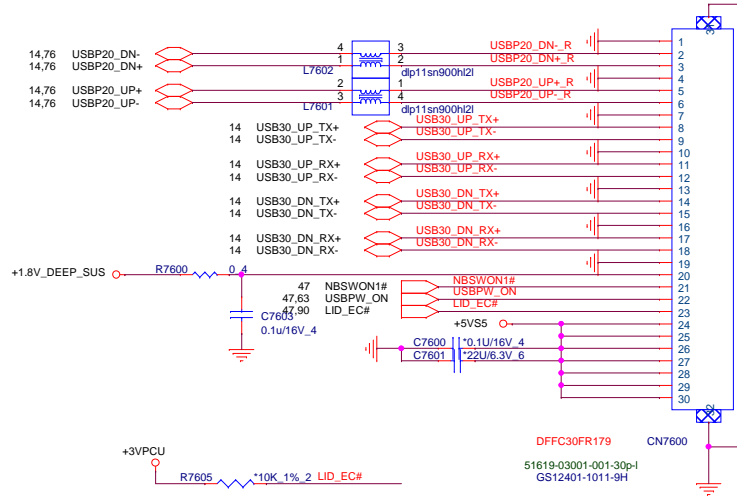
Daughter Board

SD Board

USB Board

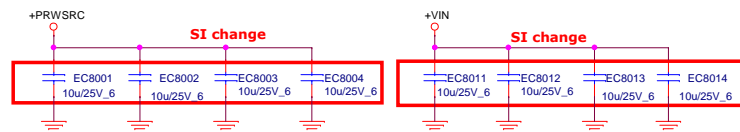
Cardreader


DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/



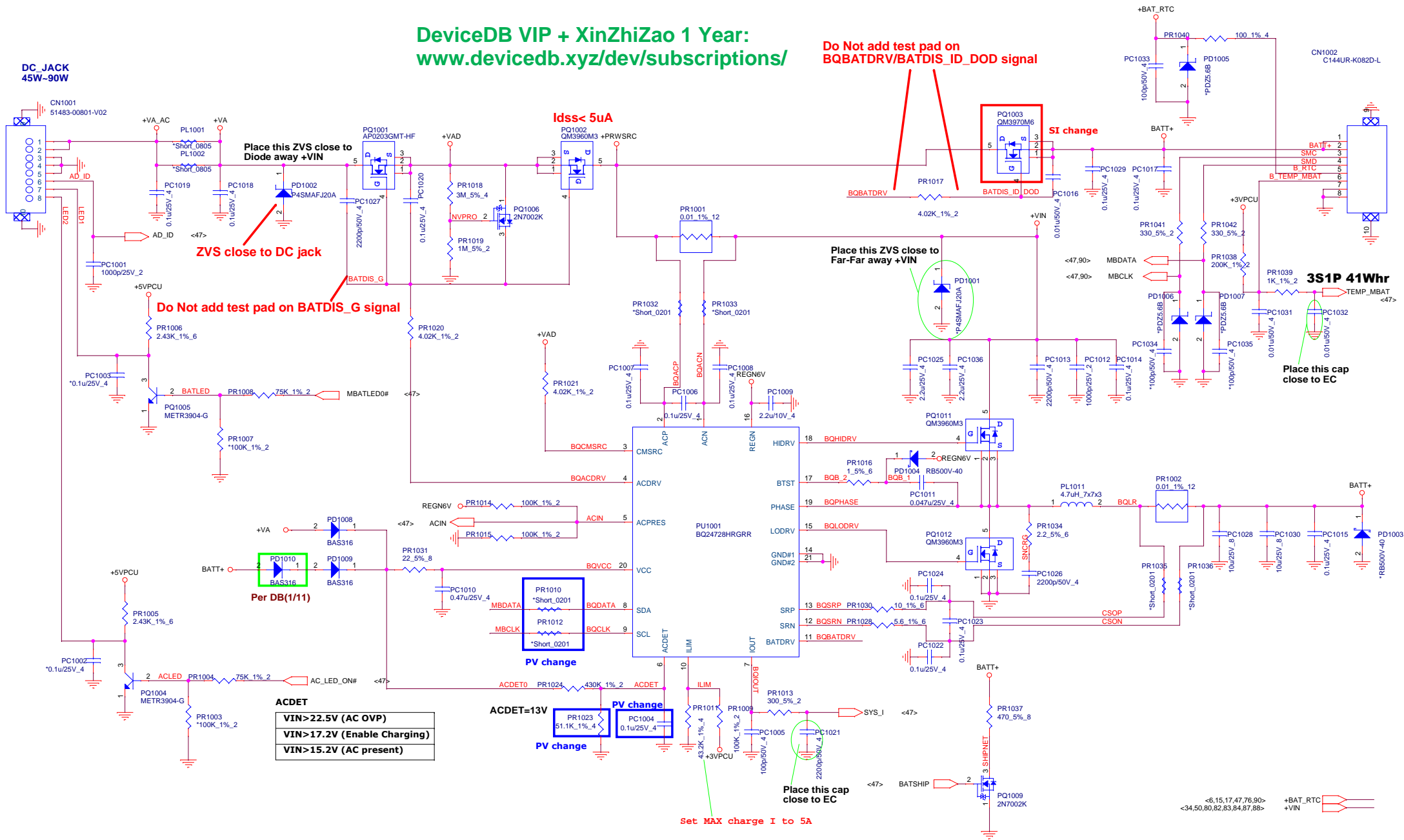
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

Reserve for EMI function test

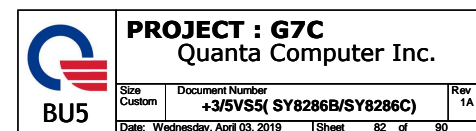


	PROJECT : G7E Quanta Computer Inc.		
	Size Custom	Document Number EMI FOR POWER RESERVE	Rev 1A
	Date: Wednesday, April 03, 2019	Sheet 80 of 90	

Do Not add test pad on
BQBATDRV/BATDIS_ID_DOD signal

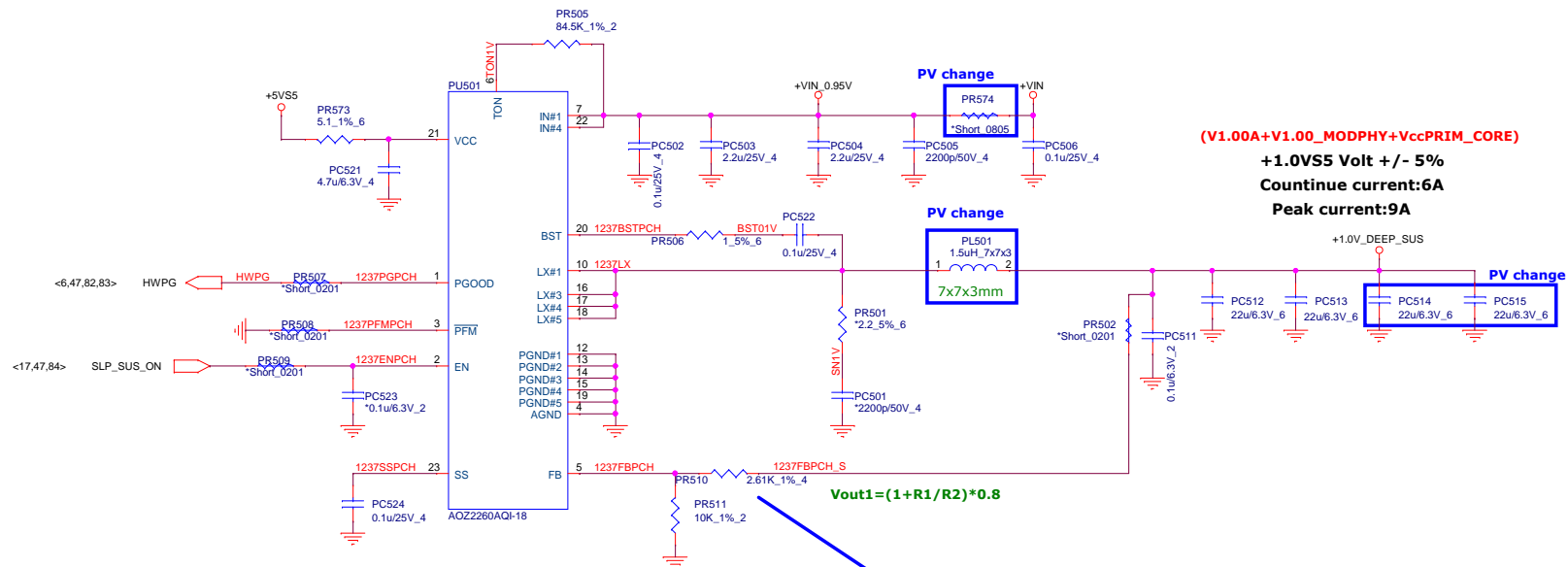


+VIN	<34,50,80,81,83,84,87,88>
+3VS5	<6,12,14,15,17,41,47,63,83,84,85,89>
+5VS5	<6,64,76,83,84,85,86,87,89>
+3VPCU	<15,36,41,47,50,54,81,90>
+5VPCU	<41,81,89>





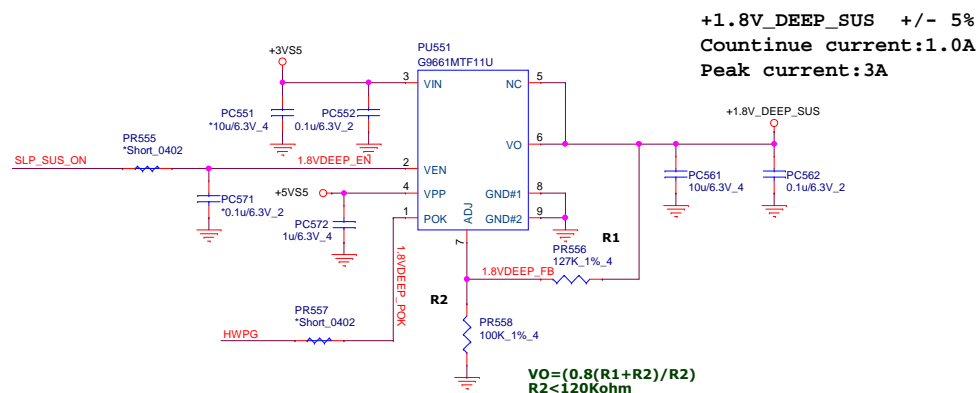
+VIN <34,50,80,81,82,83,87,88>
 +3VS5 <6,12,14,15,17,41,47,63,82,83,85,89>
 +5VS5 <6,64,76,82,83,85,86,87,89>
 +1.0V_DEEP_SUS <11,17,85>
 +1.8V_DEEP_SUS <15,17,41,76,89>
 MAINON <41,47,83,85,89>
 +1.5V



Vo	Rton
0.95V	82k
1V	84.5k
1.05V	95.3k
1.35V	113k
1.5V	127k

DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

	1.91K	CS21912FB13	0.95V
SKL/KBL	2.61K	CS22612FB15	1V
CNL/CFL/WHL	3.16K	CS23162FB04	1.05V



PROJECT : G7C
Quanta Computer Inc.

Size Custom	Document Number +1.0V/+1.8V_DEEP_SUS	Rev 1A
Date: Wednesday, April 03, 2019 Sheet 84 of 90		

SKY/KBY-U22/U42/U23e
KBY-G/WHL-U
Vcc_IO: 3.4A/1V
Stuff PU601

Stuff PU601 & merge 1V_deep_sus

Unstuff PU601

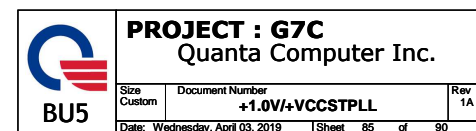
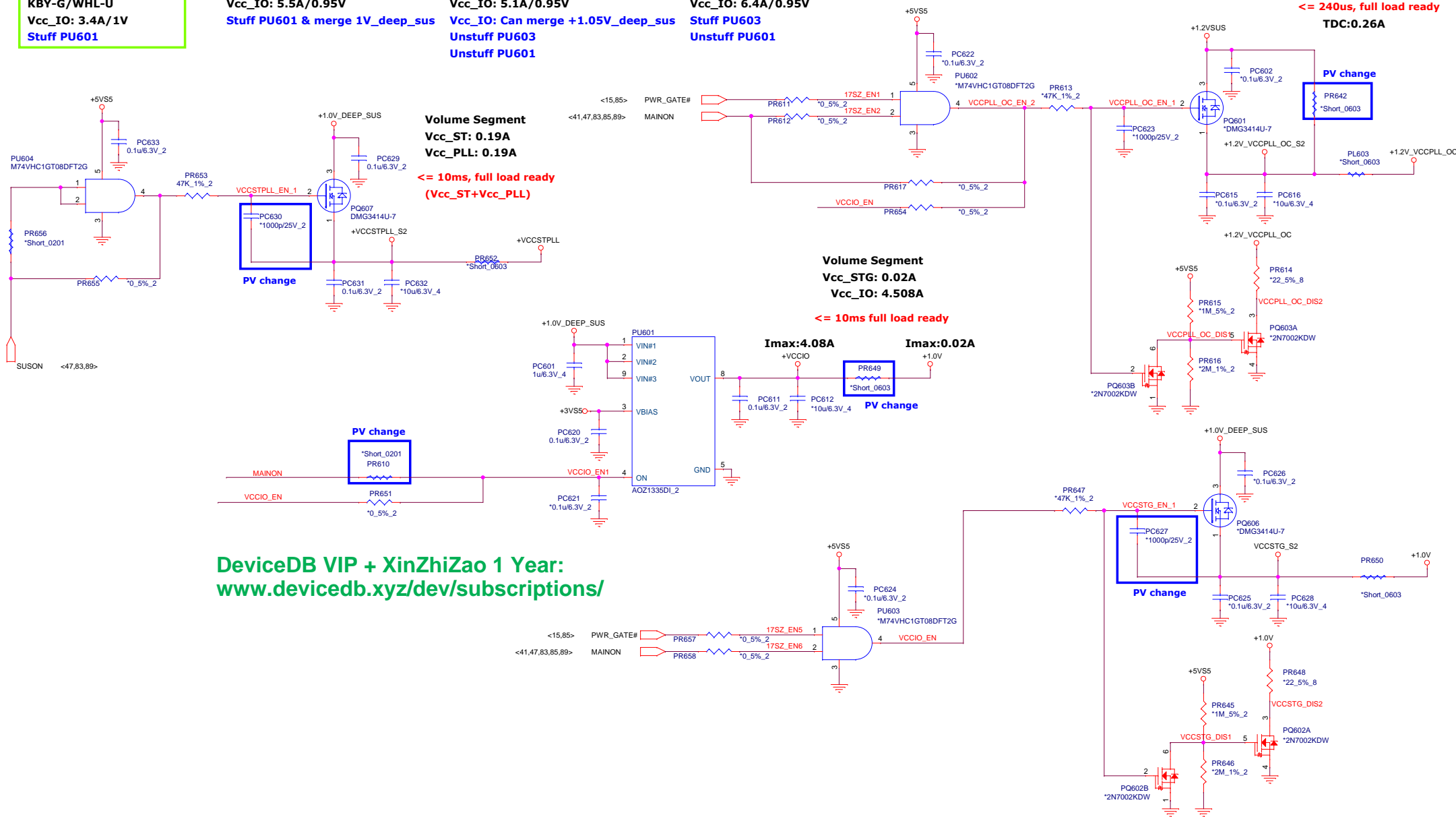
Unstuff PU601

1000

<= 10ms full load ready

Imax:0.02A

DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/



Default setting

U22	Rd
U42	
U22	Re
U42	
U22	Rf
U42	

Rg	Load Line
63.4K	-1.8mOhm/A
53.6K	-2.4mOhm/A

31A for ICCMAX=1V

5A for ICCMAX=1V

place close to core inductor


64A for ICCMAX=1V

place close to GT MOSFET

place close to VCORE MOSFET

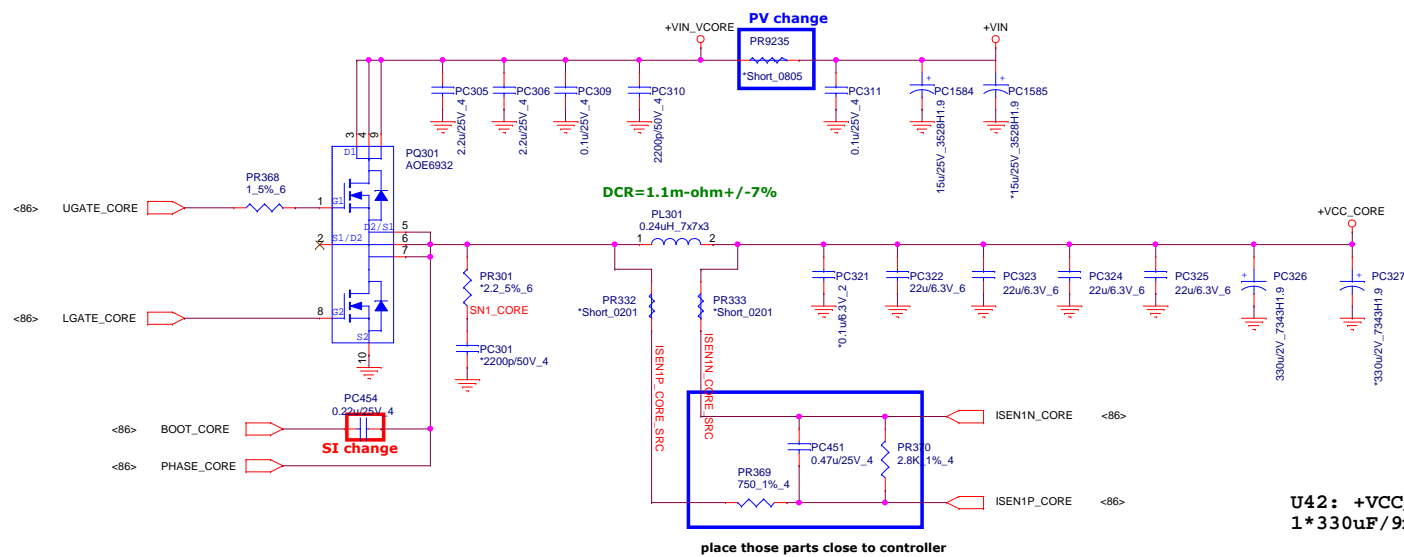
Default setting

DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

 PROJECT : G7C Quanta Computer Inc.		
Size Custom	Document Number CPU VR IC (RT3602AC)	Rev 2A
Date: Wednesday, April 03, 2019 Sheet 86 of 90		

DeviceDB VIP + XinZhiZao 1 Year: www.devedb.xyz/dev/subscriptions/

+VIN <34,50,80,81,82,83,84,88>
+5VS5 <6,64,76,82,83,84,85,86,89>



Default setting
KBL U42(15W)
CPU CORE Volt
Countinue current:42A
Peak current:64A
OCP minimum:83A
LL= -2.4mV/A
VBOOT=0V
Address=00h

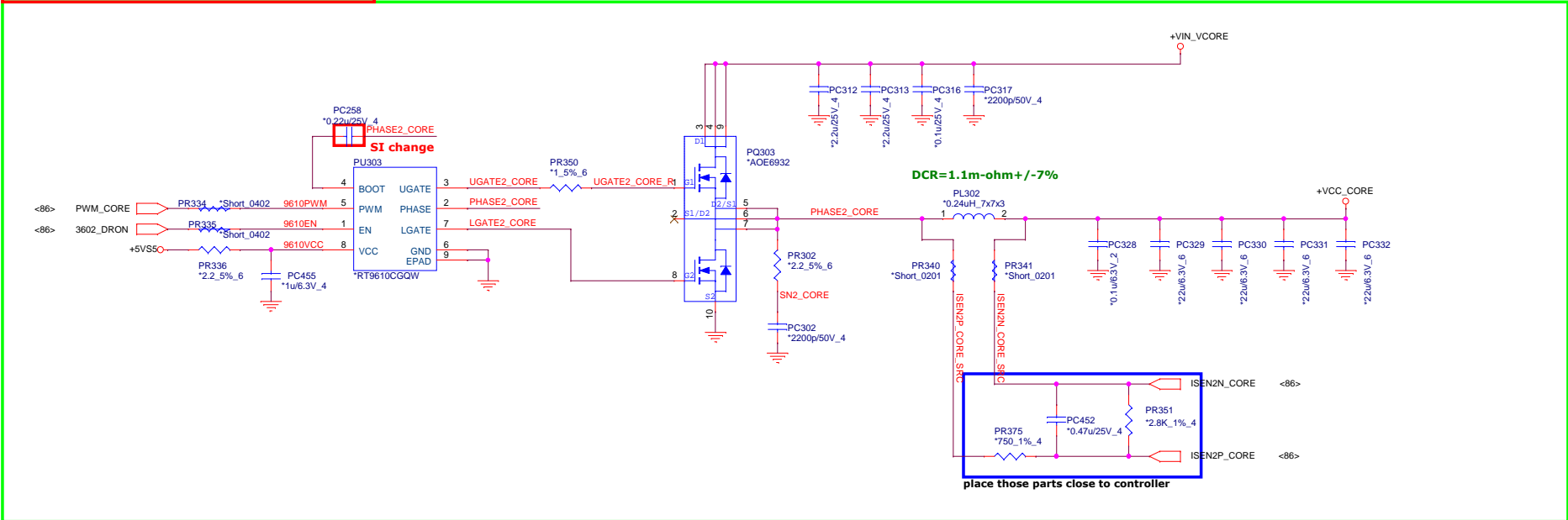
KBL U22(15W)
CPU CORE Volt
Countinue current:21A
Peak current:32A
OCP minimum:42A
LL= -2.4mV/A
VBOOT=0V
Address=00h

U42: +VCC_CORE
1*330uF/9m+30*22uF MLCC (total with EE)

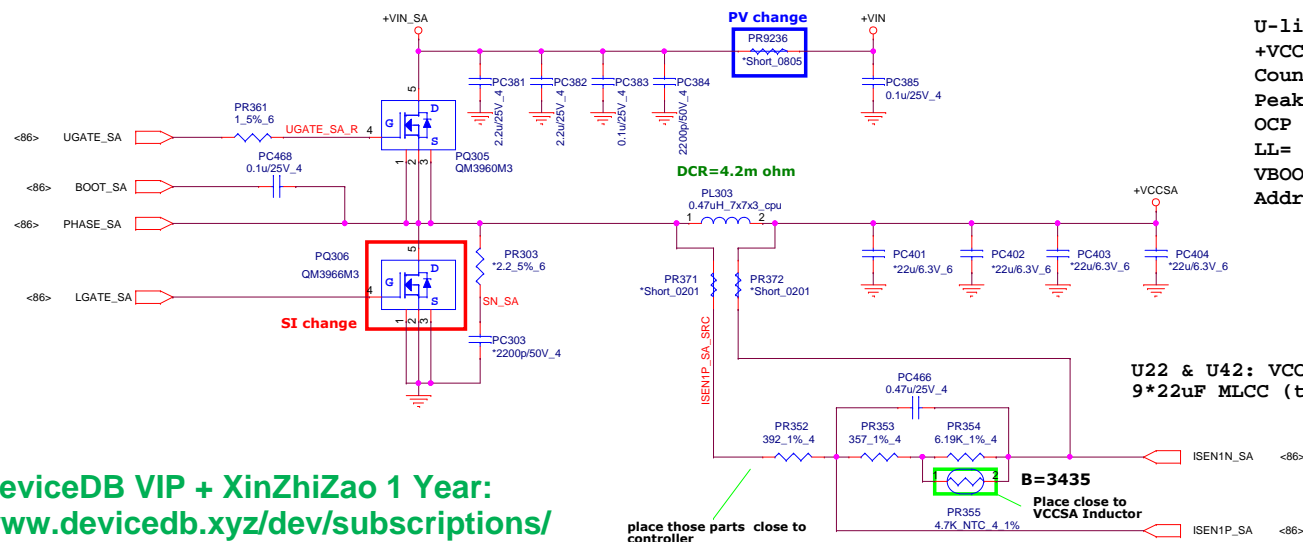
U22: +VCC_CORE
1*330uF/9m+28*22uF MLCC (total with EE)

For U42 --> Add These Components

DeviceDB VIP + XinZhiZao 1 Year:
www.device-db.xyz/dev/subscriptions/



	PROJECT : G7C	
	Quanta Computer Inc.	
	Size Custom Document Number +VCCSA (NCP81253) Date: Wednesday, April 03, 2019 Sheet 87 of 90	Rev 2A

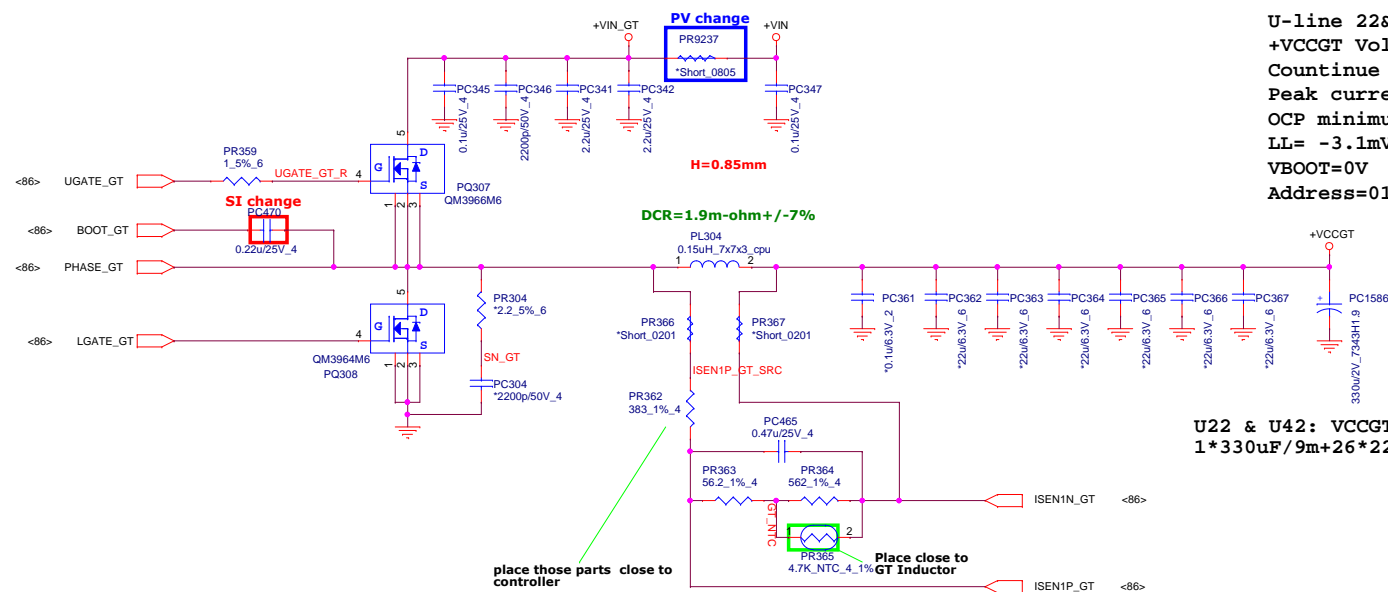


DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

```
U-line 22&42(15W)
+VCCSA Volt
Countinue current:4A
Peak current:6A
OCP minimum:8A
LL= -10.3mV/A
VBOOT=1.05V
Address=02h
```

U22 & U42: VCCSA
9*22uF MLCC (total with EE)

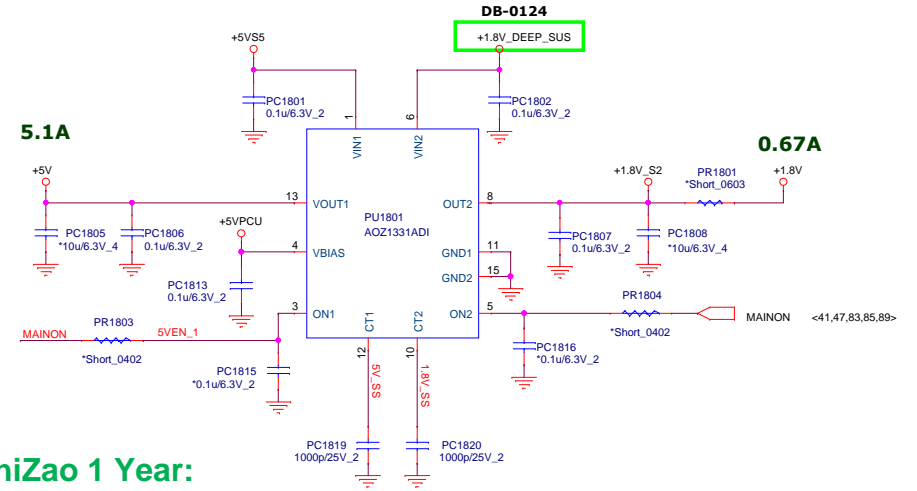
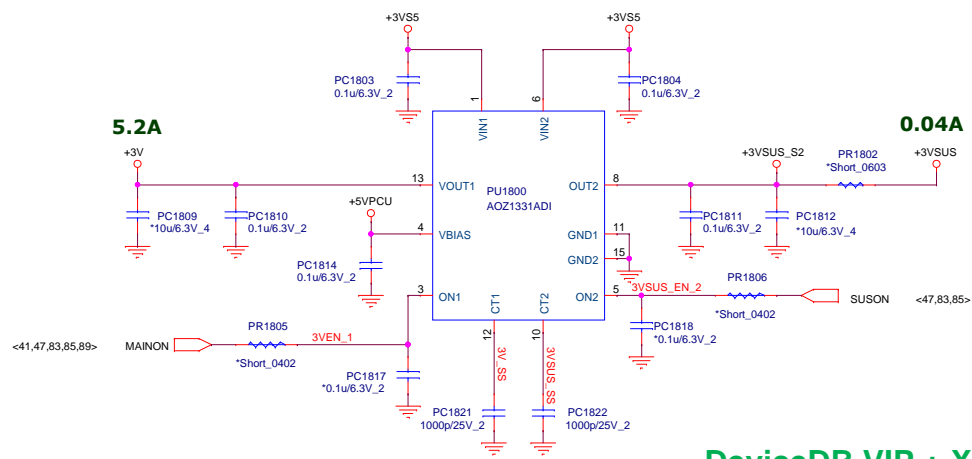
B=3435
Place close to
VCCSA Inductor



```
U-line 22&42(15W)
+VCCGT Volt
Countinue current:18A
Peak current:31A
OCP minimum:35A
LL= -3.1mV/A
VBOOT=0V
Address=01h
```

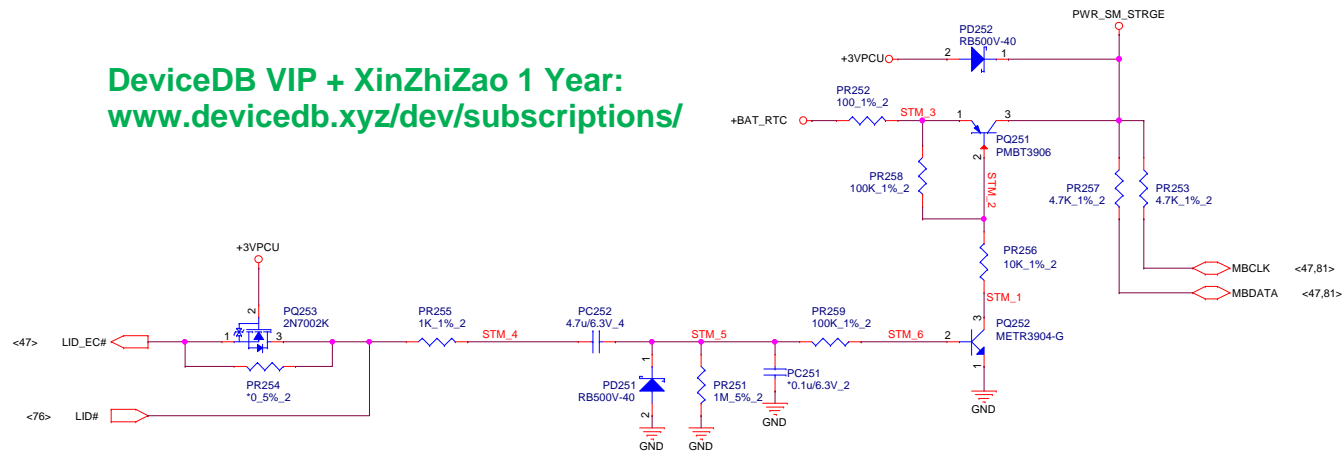
U22 & U42: VCCGT
1*330uF/9m+26*22uF MLCC (total with EE)


ISEN1P_GT <8



DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

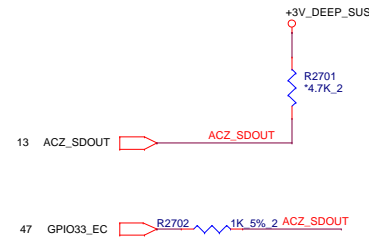
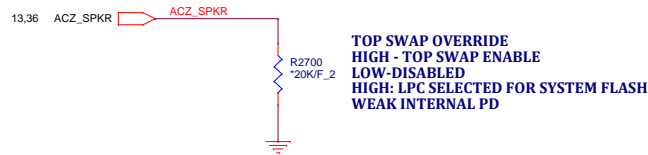
DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/



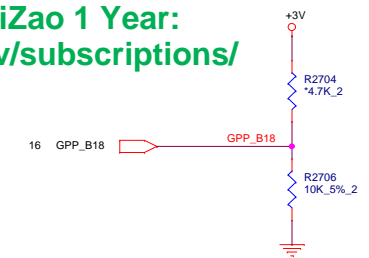
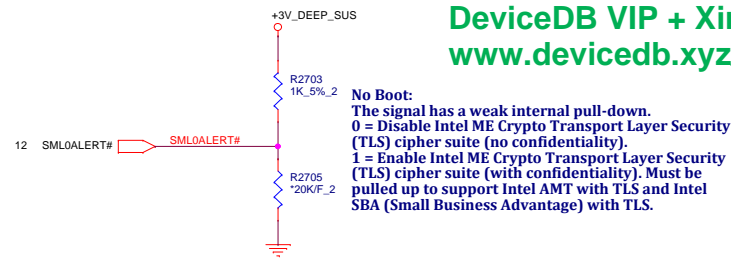
 BU5	PROJECT : G7C Quanta Computer Inc.		
	Size Custom	Document Number STORAGE MODE	Rev 1A
	Date: Wednesday, April 03, 2019	Sheet 90 of 90	

Functional Strap Definitions

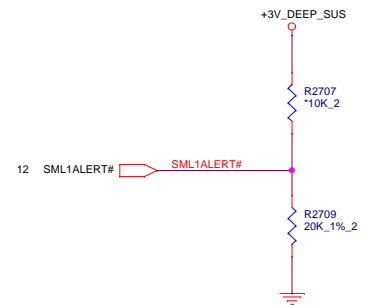
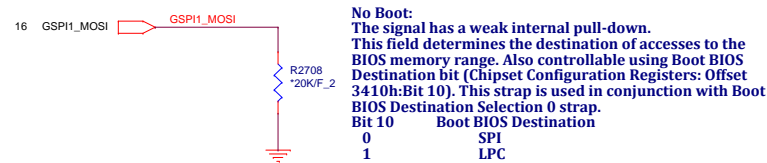
DESIGN NOTE:
WEAK PULL UP RESISTOR PRESENT ON THIS NET




No Boot:
The signal has a weak internal pull-down.
0 = Enable security measures defined in the Flash Descriptor.
1 = Disable Flash Descriptor Security (override). This strap should only be asserted high using external pull-up in manufacturing/debug environments ONLY. This function is useful when running ITP/XDP.



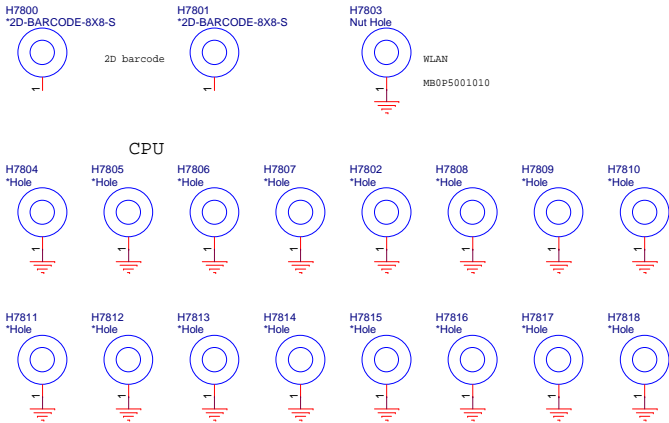
No Boot:
The signal has a weak internal pull-down.
0 = Disable No Reboot mode.
1 = Enable No Reboot mode (PCH will disable the TCO Timer system reboot feature). This function is useful when running ITP/XDP.



No Boot:
The signal has a weak internal pull-down.
0 = LPC is selected for EC.
1 = eSPI is selected for EC.

	PROJECT : OP5A		
	Quanta Computer Inc.		
	Size	Document Number	Rev
		PCH STRAP	1A
Date: Wednesday, April 03, 2019			
Sheet#7 of 90			

EMI Cap/RF Cap/HOLE



DeviceDB VIP + XinZhiZao 1 Year:
www.devedb.xyz/dev/subscriptions/

51 level BOM option 电阻

RA stuff TOP

R7802 10K_5%_2

RB stuff TOP

R7803 10K_5%_2

41 level BOM option 电阻

R7804 10K_5%_2

